

Shirpur Education Society's

R. C. Patel Institute of Technology, Shirpur (An Autonomous Institute)

Syllabus Booklet Electronics and Telecommunication Engineering

Third Year B. Tech.



Shahada Road, Near Nimzari Naka, Shirpur, Maharashtra 425405 Ph: 02563 259 802, Web: www.rcpit.ac.in

	Third Year B.Tech Electronics & Telecommunication Engg. Semester-V (w.e.f.2024-2025)												
	6			Tea Sch	achir eme	ıg		Evaluation Scheme					
Sr	Course Category	Course Code	Course Title				Cor	Continuous Assessment (CA)			Total	Credit	
							ТА	Term Test 1 (TT1)	Term Test 2 (TT2)	Best of (TT1&T T2)	ESE		
				L	Т	Р	[A]			[B]	[C]	[A+B+C]	
1	РС	22PCET5010T	Analog Communication	3			20	15	15	15	65	100	3
2	РС	22PCET5010L	Analog Communication Laboratory			2	25				25	50	1
3	РС	22PCET5020T	Radio Frequency Circuit Design	3			20	15	15	15	65	100	3
4	РС	22PCET5020L	Radio Frequency Circuit Design Laboratory			2	25				25	50	1
5	РС	22PCET5030T	Microcontroller & Applications-II	3			20	15	15	15	65	100	3
6	РС	22PCET5030L	Microcontroller & Applications-II Laboratory			2	25				25	50	1
7	PC	22PCET5040T	Digital Signal Processing	3			20	15	15	15	65	100	3
8	РС	22PCET5040L	Digital Signal Processing Laboratory			2	25				25	50	1
9	РС	22PCET5050T	Data Structures & Algorithms	1			20	15	15	15		35	1
10	РС	22PCET5050L	Data Structures & Algorithms Laboratory			2	25				25	50	1
11	PJ	22PJET5060L	Semester Project-III			2	25				25	50	1
12	НМ	22HMET5070L	Employability Skill Development Program-II			2	50					50	1
13	MC	22MCET5080T	Environmental Engineering	1									Audit
	Total 14 14 300 75 75 410 785 20												

	Third Year B.Tech Electronics & Telecommunication Engg. Semester-VI (w.e.f.2024-2025)												
	6			Teaching Scheme		g		Eva	aluation	Scheme			
Sr	Course Categor	Course Code	Course Title				Сог	ntinuous .	Assessme	nt (CA)			
	У						ТА	Term Test 1 (TT1)	Term Test 2 (TT2)	Best of (TT1& TT2)	ESE	Total	Credit
				L	Т	Р	[A]			[B]	[C]	[A+B+C]	
1	PC	22PCET6010T	Digital Communication	3			20	15	15	15	65	100	3
2	РС	22PCET6010L	Digital Communication Laboratory			2	25				25	50	1
3	PC	22PCET6020T	Radiating Systems	3			20	15	15	15	65	100	3
4	PC	22PCET6020L	Radiating Systems Laboratory			2	25				25	50	1
5	PC	22PCET6030T	Computer Networks	3			20	15	15	15	65	100	3
6	PC	22PCET6030L	Computer Networks Laboratory			2	25				25	50	1
7	РС	22PCET6040T	Fundamentals of Digital Image Processing	3			20	15	15	15	65	100	3
8	РС	22PCET6040L	Fundamentals of Digital Image Processing Laboratory			2	25				25	50	1
9@	ÞF	22PEET605-T	Professional Elective Course	3			20	15	15	15	65	100	3
10	1 L	22PEET605-L	Professional Elective Course Laboratory			2	25				25	50	1
11	НМ	22HMET6060T	Professional & Business Communication Tutorial		2		50					50	2
12	PJ	22PJET6070L	Project Stage - I			2	25				25	50	2
	Total 15 2 12 300 75 75 475 850 24												

@ Any one Elective Course





Semester-VI-Professional Elective Courses						
Sr. No.	Course Code	Course Title				
1	RCP22PEET6051	Basic VLSI				
2	RCP22PEET6052	Control Systems				
3	RCP22PEET6053	Neural Network & Fuzzy Logic				
4	RCP22PEET6054	Operating Systems				
5	RCP22PEET6055	Big Data Analytics				
6	RCP22PEET6056	Radar Engineering				
7	RCP22PEET6057	Linear Algebra				

Prepared by Mh (BVP)

Checked by

BOS Chairman

Dean Academic/Dy. Director Deputy Director R. C. Patel Institute of Technology Shirpur, Dist. Dhule (MS)

Controller of Examination R.C.Patel Institute of Technology Shirpur Dist.Dhule 425 405

Director DIRECTOR C Patel Institute of Technology Shirour Dist Dhule (MS)





Shirpur Education Society's

R. C. Patel Institute of Technology, Shirpur (An Autonomous Institute)



Syllabus Booklet Electronics and Telecommunication Engineering

Third Year B.Tech Semester-V

With Effect from Academic Year 2024-25



Shahada Road, Near Nimzari Naka, Shirpur, Maharashtra 425405 Ph: 02563 259802, Web: www.rcpit.ac.in

Analog Communication (22PCET5010T)

Teaching Scheme Lectures: 03 Hrs./Week Credit: 03

Examination Scheme

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite

1.Electronics Circuit Design 2.Signals Systems

Course Objectives

1. To understand basics of communication systems and effect of noise on communication

2. To understand various continuous and pulse modulation, demodulation techniques

3. Get acquainted with various types of multiplexing techniques and their use in communication

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	To compare internal and external noise and its effect on commu- nication system.	L2	Comprehension
CO2	To examine analog modulation and demodulation techniques along with various analog receivers.	L4	Analyze
CO3	To make use of sampling theorem to analog and digital pulse mod- ulation and demodulation techniques.	L4, L5	Analyze, Syn- thesize
CO4	To compare Frequency division and time division multiplexing and de-multiplexing techniques for communication system.	L3	Evaluate



Unit-I Basics of Communication System 04 Hrs.

Block diagram, electromagnetic spectrum, signal bandwidth and power, types of communication channels, Introduction to time and frequency domain; Types of noise, signal to noise ratio, noise figure and noise temperature

Unit-II Amplitude Modulation and Demodulation 12 Hrs.

Introduction, need for modulation

DSBFC: Mathematical analysis, modulation index, bandwidth, voltage distribution and power calculations. Low level and high level modulation, simple diode detector, practical diode detector.

DSBSC: Mathematical analysis, modulation index, bandwidth, voltage distribution, power calculations, balanced modulator.

SSBSC: Mathematical analysis, voltage distribution and power calculations

SSB generation: Filter method.

ISB: Transmitter and receiver block diagram, applications.

VSB: Application in television

Unit-III Angle Modulation and Demodulation 10 Hrs.

Frequency modulation (FM): Introduction, mathematical analysis, time domain waveform, spectrum of FM wave, modulation index, bandwidth requirement, narrowband FM and wideband FM, Effect of noise, noise triangle, pre-emphasis and de-emphasis, FET reactance modulator, varactor diode modulator, frequency stabilized reactance modulator, indirect method of FM generation. Comparison between FM and PM, FM demodulation: Balance slope detector, Foster-Seely discriminator, ratio detector, amplitude limiting and thresholding.

Unit-IV Radio Receivers 04 Hrs.

Receiver parameters, TRF receiver, problems in TRF receiver, Super - heterodyne receiver, choice of IF, Comparison of FM receiver with AM receiver.

Unit-V Pulse Modulation & Demodulation 06 Hrs.

Sampling theorem, Nyquist criteria. Sampling techniques, aliasing error and aperture effect PAM, PWM, PPM generation and detection, Quantization and its types, Pulse Code Modulation, delta modulation, adaptive delta modulation, principle, generation and detection. Applications of pulse communication.



Unit-VI Multiplexing & De-Multiplexing 04 Hrs.

Frequency Division Multiplexing transmitter & receiver block diagram, Time Division Multiplexing transmitter & receiver block, Examples and applications of FDM and TDM

Text Books

- Kennedy and Devis, "Electronic Communication System", McGraw Hill Education Pvt. Ltd., Fourth Edition, 2017
- 2. Wayne Tomasi, "Electronic Communication System", Pearson, Fifth Edition, 2012.

Reference Books

- 1. Toub Schilling and Shaha, "Principles of Communication System", Tata McGraw Hill, Fourth Edition
- B. P. Lathi, Zhi Ding, "Modern digital and analog communication system", Oxford University Press, Fourth Edition.
- 3. Symon Haykin, Michal Moher, "Introduction to Analog and Digital Communication", Wiley, Fourth Edition.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of the marks scored in both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.



Analog Communication Laboratory (22PCET5010L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01 **Examination Scheme**

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

- 1. To understand basics of communication systems and effect of noise on communication
- 2. To understand various continuous and pulse modulation, demodulation techniques
- 3. Get acquainted with various types of multiplexing techniques and their use in communication

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	To compare internal and external noise and its effect on commu- nication system.	L2	Comprehension
CO2	To examine analog modulation and demodulation techniques along with various analog receivers.	L4	Analyze
CO3	To make use of sampling theorem to analog and digital pulse mod- ulation and demodulation techniques.	L4, L5	Analyze, Syn- thesize
CO4	To compare Frequency division and time division multiplexing and de-multiplexing techniques for communication system.	L3	Evaluate



List of Laboratory Experiments: (Any Eight)

- 1. Study of Amplitude Modulation
- 2. Study of Double Side Band Suppressed Carrier and Single Side Band Amplitude Modulation
- 3. Simulate of AM system and generate time and frequency domain output
- 4. To study different types of frequency modulators and Demodulators
- 5. Simulate Frequency Modulation system and generate time and frequency domain output.
- 6. Implement Pre-emphasis and De-emphasis circuit required for FM and analyze the output
- 7. Study of Natural Sampling and its reconstruction.
- 8. Study of Flat top sampling and its reconstruction
- 9. Study of Pulse Amplitude Modulation
- 10. Study of Pulse width Modulation.
- 11. Study of Pulse Position Modulation.
- 12. Study of PAM-TDM system.
- 13. Study of FDM.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination will be based on the entire syllabus including, the practicals performed during laboratory sessions.



Radio Frequency Circuit Design (22PCET5020T)

Teaching Scheme

Lectures: 03 Hrs./Week Credit: 03 Examination

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite

- 1. Electromagnetics Wave Propagation
- 2. Electrical Network Analysis and Synthesis
- 3. Engineering Mathematics-III

- 1. To develop the model for inductor, capacitor and resistor at high frequency.
- 2. To analyse transmission line using Smith Chart.
- 3. To study application of smith chart for impedance matching.
- 4. To synthesize filter for given specifications.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Analyze the single and Multi-port network using parameters.	L3	Evaluate
CO2	Apply their knowledge in analyzing inductor, capacitor and resistor at high frequency.	L2	Comprehension
CO3	Calculate parameters of transmission line analytically and using Smith Chart.	L4	Analyze
CO4	Design matching network using impedance matching techniques.	L4, L5	Analyze, Syn- thesize
CO5	Design the filters for given specifications using insertion loss and image parameter method.		Evaluate



Unit-I Single and Multiport Networks 08 Hrs.

Basic Definitions

Interconnecting Networks, Series Connection of Networks, Parallel Connection of Networks, Cascading Networks

The Scattering Matrix

Reciprocal Networks and Lossless Networks, A Shift in Reference Planes, Power Waves and Generalized Scattering Parameters, Practical Measurements of S-Parameters

The Transmission (ABCD) Matrix, Relation to Impedance Matrix and Scattering Matrix, Equivalent Circuits for Two-Port Networks.

Unit-II Importance of Radio Frequency Design 08 Hrs.

RF behaviour of Passive Components

High-Frequency Resistors, High-Frequency Capacitors, High-Frequency Inductors

Chip Components and circuit Board Considerations

Chip Resistors, Chip Capacitors, Surface-Mounted Inductors

SMD Assembly Process

Solders for SMD Applications, Fluxing and Cleaning, Types of Flux- Organic Soluble Fluxes, R Flux, RMS Flux, RA Flux, Water Soluble Fluxes and Types Flux Selection, Solder Applications, Curing solder Paste, The Reflow Process, Assembly Methods, Adhesive Applications and Curing, Solder Creams

Unit-III Smith Chart 10 Hrs.

From Reflection Coefficient to Load Impedance

Reflection coefficient in Phasor Form, Normalised Impedance Equation, Parametric Reflection Coefficient Equation, Graphical Representation

Impedance Transformation

Impedance Transformation for General Load, Standing Wave Ratio, Special Transformation Conditions

Admittance Transformation

Parametric Admittance Equation, Additional Graphical Displays

Z-Y Smith Chart

Parallel and Series Connection of Lumped Elements and their analysis using Smith Chart

Parallel Connection of R and L, Parallel Connection of R and C, Series Connection of R and L, Series Connection of R and C, T and π Network.



Unit-IV Impedance Matching and Tuning

Matching with Lumped Elements (L Networks)

Analytic Solutions, Smith Chart Solutions

Impedance Transformers

Single-Section Quarter-Wave Transformer, Multi-section Quarter-Wave Transformer, Transformers with Uniformly distributed section reflection coefficient, Binomial Multisection Matching Transformer, Chebyshev Multi-section Matching Transformer

Unit-V RF Filter Design 08 Hrs.

Basic Resonator and Filter configurations

Filter Types and Parameters, Low-Pass Filter, High-Pass Filter, Bandpass and Bandstop Filters, Insertion Loss

Special Filter Realizations using Insertion Loss Method

Butterworth-Type Filters, Chebyshev-Type Filters, Denormalization of Standard LowPass Design

Filter Implementation

Unit Elements, Kuroda's Identities, Microstrip Filter Design

Filter Design by the Image Parameter Method

Image Impedances and Transfer Functions for Two-Port Networks, Constant-k Filter sections, mderived Filter Sections, Composite Filters.

Text Books

- Ludwig, Reinhold & Bretchko, Pavel (2007). RF Circuit Design: Theory and Applications, 2nd Edition, Prentice-Hall, Upper Saddle River, N.J..
- 2. Pozar, David M. (2012). Microwave Engineering. Hoboken, NJ: Wiley Publication.
- 3. Traister, John (2012). Design Guidelines for Surface Mount Technology, Elsevier.

Reference Books

- Guillermo Gonzalez. (1996). Microwave Transistor Amplifiers 2nd Edition: Analysis and Design. Prentice Hall, Inc., USA.
- Matthew M. Radmanesh, "Radio Frequency and Microwave Electronics Illustrated", Prentice Hall PTR, 2001.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester. Continuous Assessment (B):





- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of the marks scored in both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.



Radio Frequency Circuit Design Laboratory (22PCET5020L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01 **Examination** Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

- 1. To develop the model for inductor, capacitor and resistor at high frequency.
- 2. To analyse transmission line using Smith Chart.
- 3. To study application of smith chart for impedance matching.
- 4. To synthesize filter for given specifications.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Analyze the single and Multi-port network using parameters.	L3	Evaluate
CO2	Apply their knowledge in analyzing inductor, capacitor and resistor at high frequency.	L2	Comprehension
CO3	Calculate parameters of transmission line analytically and using Smith Chart.	L4	Analyze
CO4	Design matching network using impedance matching techniques.	L4, L5	Analyze, Syn- thesize
CO5	Design the filters for given specifications using insertion loss and image parameter method.		Evaluate



List of Laboratory Experiments: (Any Eight)

- 1. Characterisation of resistor at high frequency.
- 2. Characterization of capacitor at high frequency
- 3. Characterisation of inductor at high frequency.
- 4. Analysis of Parallel and Series Connection of Lumped Elements and verification using Smith chart.
- 5. Filter Design by the Image Parameter Method.
- 6. Filter Design by the Insertion Loss Method.
- 7. Matching of Lumped Elements.
- 8. Design of quarter wave transformer.
- 9. Design of Binomial Multi-Section Matching Transformer.
- 10. Numerical from previous years GATE Examination paper.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination will be based on the entire syllabus including, the practicals performed during laboratory sessions.



Microcontroller & Applications-II (22PCET5030T)

Teaching Scheme

Lectures: 03 Hrs./Week Credit: 03 Examination

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite

Knowledge of

- 1. Digital System Design
- 2. Microcontroller & Applications I

- 1. To develop background knowledge and core expertise in microcontrollers.
- 2. To understand peripheral devices and their interfacing to microcontrollers.
- 3. To develop programming skill for microcontroller and their applications in Assembly and Embedded C language

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Identify different functionalities and architecture of ARM 7 Processor	L2	Comprehension
CO2	Identify different hardware components and use relevant software for programming of LPC2148 microcontroller-based development system.	L2	Comprehension
CO3	Write assembly language programing and Embedded C program- ming for LPC2148 microcontroller-based systems.	L2, L3	Comprehension, Apply
CO4	Interface different input/output devices with LPC2148 microcon- troller for various applications		Apply



Unit-I ARM7 Architecture 05 Hrs.

Features of ARM core architecture, Data Flow Model, Pipeline, Registers, operating modes

Unit-II Introduction to ARM Programming 10 Hrs.

Introduction to THUMB, Differences between ARM and THUMB, Register usage in Thumb, ARM Thumb Interworking. General Structure of ARM assembly module, Assembler directives- Simple ALP programs on Arithmetic & logical operations, Factorial, string operation, sorting, searching, and Scan

Unit-III LPC2148 ARM CPU 10 Hrs.

Salient features, Pin diagram, block diagram, memory mapping. Functional features of Interrupt controller, RTC, USB, UART, I2C, SPI, SSP controllers, watch dog timers and other system control unit

Unit-IV LPC2148 Peripherals 08 Hrs.

Registers, GPIOs, PLL-Features, PLL structure, TimersFeatures, applications, Architecture of timer module, register description, Simple C programs for application using -GPIO, PLL, Timer.

Unit-V LPC2148 based Applications 07 Hrs.

Design of system using GPIO's Blink a group of 8 LEDs with a delay, Stepper motor control, DC motor control, LCD interface, ADC, DAC, UART.

Text Books

- 1. Andrew N. Sloss, "ARM System Developers Guide", Elsevier, First Edition, 2008.
- Lyla Das, "Embedded Systems: An Integrated Approach", Pearson Publication, First Edition, 2012

Reference Books

- William Hohl, "ARM Assembly Language Fundamentals and Techniques", CRC Press, First Edition, 2009.
- 2. Steve Furber, "Arm System On Chip Architecture", Pearson Publication, First Edition, 2012
- J.R. Gibson, "ARM Assembly Language: An Introduction", Cengage Learning, First Edition, 2010.



Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of the marks scored in both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.



Microcontroller & Applications-II Laboratory(22PCET5030L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01 **Examination Scheme**

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

- 1. To develop background knowledge and core expertise in microcontrollers.
- 2. To understand peripheral devices and their interfacing to microcontrollers.
- 3. To develop programming skill for microcontroller and their applications in Assembly and Embedded C language

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Identify different functionalities and architecture of ARM 7 Processor	L2	Comprehension
CO2	Identify different hardware components and use relevant software for programming of LPC2148 microcontroller-based development system.	L2	Comprehension
CO3	Write assembly language programing and Embedded C program- ming for LPC2148 microcontroller-based systems.	L2, L3	Comprehension, Apply
CO4	Interface different input/output devices with LPC2148 microcon- troller for various applications	L3	Apply



List of Laboratory Experiments: (Any Eight)

- 1. To study ARM (LPC2148) Embedded Trainer kit and its software tools.
- 2. Write a program to generate LED sequence using ARM 7(LPC2148).
- 3. To display message on multiplexed four common anode7-segment display.
- 4. To read DIP switch status and display its position on 7-segment.
- 5. To display message on Alphanumeric LCD
- 6. To verify LDR operation using on-chip ADC of LPC2148.
- 7. Waveforms generation using DAC
- 8. To display room temperature on LCD using LM35 sensor
- 9. Write a program to control DC motor speed using PWM
- 10. Write a program for Interfacing keyboard and LCD
- 11. Write a program for Interfacing EPROM and EEPROM
- 12. Write a program for Interfacing stepper motor.
- 13. Write a program to transmit and receive data serially using UART.
- 14. Implementing ZIGBEE protocol with ARM.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks



The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination will be based on the entire syllabus including, the practicals performed during laboratory sessions.



Digital Signal Processing (22PCET5040T)

Teaching Scheme Lectures: 03 Hrs./Week Credit: 03

Examination Scheme

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite: 1. Signals and Systems

Course Objectives

1. To develop a thorough understanding of DFT and FFT and their applications.

2. To apply the design techniques and performance analysis of digital filters.

3. To understand the effects of Poles and Zeros in the frequency response of digital filters.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Implement DFT and FFT algorithms in finding the response of the system.	L2, L3	Understand, Apply
CO2	Design different types of IIR filters.	L4	Analyze
CO3	Design different types of FIR filters.	L4	Analyze
CO4	Evaluate the effects of Poles and Zeros in design of digital filters.	L3, L4	Apply, Analyze



Unit-I Discrete Fourier Transform & Fast Fourier Transform 10 Hrs.

Definition and Properties of DFT, IDFT, Circular convolution of sequences using DFT and IDFT. Filtering of long data sequences: Overlap-Save and Overlap-Add Method for computation of DFT. Fast Fourier Transforms (FFT), Radix-2 decimation in time and decimation in frequency FFT algorithms, inverse FFT, composite Radix FFT N=2.3, N=3.2.

Unit-II IIR Digital Filters 10 Hrs.

Types of IIR Filters (Low Pass, High Pass, Band Pass, Band Stop), Analog filter approximations: Butterworth, Chebyshev I. Mapping of S-plane to Z-plane, impulse invariance method, bilinear transformation method, Design of IIR digital filters (Butterworth and Chebyshev-I) from Analog filters with numerical examples. Effect of Poles and Zeros on the Frequency Response of IIR filters. Position of Poles and Zeros of Low Pass, High Pass, Band Pass, Band Stop, All Pass filters.

Unit-IIIFIR Digital Filters08 Hrs.

Characteristics of FIR digital filters, Minimum Phase, Maximum Phase, Mixed Phase and Linear Phase (Type 1 to Type 4) FIR Filters. Design of FIR filters using Window techniques (Rectangular, Hamming, Hanning, Blackman), Design of FIR filters using Frequency Sampling technique, Comparison of IIR and FIR filters.

Unit-IV Poles, Zeros and Filters 06 Hrs.

Effects of poles and zeros in the frequency response of IIR filters (LP, HP, BP, BR/Notch, All Pass filters). Placement of zeros and design of filters in Type1 to Type 4 Linear Phase FIR filters.

Finite Word Length effects in Digital Filters

Quantization, truncation and rounding, Error due to truncation and rounding.

Unit-V DSP Processors 06 Hrs.

Introduction to General Purpose and Special Purpose DSP processors, fixed point and floatingpoint DSP processor, Computer architecture for signal processing, Harvard Architecture, Pipelining, multiplier and accumulator (MAC), Special Instructions, Special purpose DSP hardware, Architecture of TMS320CX fixed and floating DSP processors.

Text Books

1. Proakis J., Manolakis D., Digital Signal Processing, 4st Edition, Pearson Education.

2. B. Venkata Ramani and M. Bhaskar, Digital Signal Processors, Architecture, Programming and

Applications, Tata McGraw Hill, 2004.

Reference Books

- Oppenheim A., Schafer R., Buck J., Discrete Time Signal Processing, 2st Edition, Pearson Education.
- 2. Sanjit K. Mitra, Digital Signal Processing A Computer Based Approach, 4th Edition McGraw Hill Education (India) Private Limited.
- 3. Tarun Kumar Rawat, Digital Signal Processing, Oxford University Press, 2015.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of the marks scored in both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.



Digital Signal Processing Laboratory (22PCET5040L)

Teaching	Scheme
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Practical: 02 Hrs/Week Credit: 01 **Examination Scheme**

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

- 1. To develop a thorough understanding of DFT and FFT and their applications.
- 2. To apply the design techniques and performance analysis of digital filters.
- 3. To understand the effects of Poles and Zeros in the frequency response of digital filters.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Implement DFT and FFT algorithms in finding the response of the system.	L2, L3	Understand, Apply
CO2	Design different types of IIR filters.	L4	Analyze
CO3	Design different types of FIR filters.	L4	Analyze
CO4	Evaluate the effects of Poles and Zeros in design of digital filters.	L3, L4	Apply, Analyze



List of Laboratory Experiments: (Any Eight)

- 1. Plot of Discrete Time Signals.
- 2. Frequency response of LTI systems by DTFT.
- 3. To perform Discrete Fourier Transform.
- 4. To implement Circular Convolution of two discrete time sequences.
- 5. To perform Overlap Add method of DFT for long data sequence.
- 6. To implement the algorithm of DIT-Fast Fourier Transform.
- 7. To plot the FFT of Sinusoids with noise.
- 8. Magnitude and phase response of FIR filter.
- 9. Design an Analog Butterworth filter with given specifications.
- 10. Design a Digital IIR Butterworth filter with given specifications.
- 11. Design an FIR filter by window method.
- 12. Removal of Noise by a designed filter.
- 13. Perform basic signal processing operations with DSP processor TMS 320C6713.
- 14. Generation of Sine Wave with Key Pressing in DSP processor TMS 320C6713
- 15. Capturing a real time signal by the Processor and display on a DSO.
- 16. Implementation of Real time Low Pass filtering by DSP Processor.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination will be based on the entire syllabus including, the practicals performed during laboratory sessions.



Data Structures & Algorithms (22PCET5050T)

Teaching Scheme

Lectures : 01 Hrs./week Credit: 01 Examination Scheme

Term Test (Th) : 15 Marks Teacher Assessment : 20 Marks Total: 35 Marks

Pre-requisite:

1. Structured programming using C

- 1. Understand and remember algorithms and its analysis procedure.
- 2. Introduce the concept of data structures through ADT including List, Stack, Queues.
- 3. To design and implement various data structure algorithms.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Design and implement various data structures such as arrays, linked lists, stacks, queues and trees	L2	Comprehension
CO2	Understand the operations of data structures	L4	Analyze
CO3	Determine and analyze the complexity of given Algorithms.	L4, L5	Analyze, Syn- thesize



Unit-I Introduction to Data structures and Algorithms 02 Hrs.

Introduction to Data structures, Types of Data structures: Linear and nonlinear data structures, Arrays, Stacks, Queue, Linked list and Tree, Recursion

Unit-II Stack and Queue data structure 04 Hrs.

Introduction to Stack, Operations on Stack Introduction to Queue, Queue as ADT, Operations on Queue, Linear representation of queue, Circular Queue.

Unit-III Linked List data structure 04 Hrs.

Introduction to Linked List, Singly Linked list, Doubly Linked list, Operations on linked list, Linked representation of stack, Linked representation of Queue.

Unit-IVTree04 Hrs.Introduction to Trees, Definitions & Tree terminologies, Binary tree representation, Operations on
binary tree, Traversal of binary trees, Binary search tree.

Text Books

- Tenenbaum, Langsam, Augenstein, "Data structures using C", Pearson Education, First Edition, 2019
- Ellis Horowitz, Sartaj Sahni and Susan Anderson-Freed, "Fundamentals of Data Structures in C", W. H. Freeman and Company, Second Edition, 2008.
- 3. Reema Thareja, "Data Structures using C", Oxford, Second Edition, 2017.

Reference Books

- Mark A. Weiss, "Data Structures and Algorithm Analysis in C", Pearson Education, Fourth Edition, 2014.
- M. T. Goodritch, R. Tamassia, D. Mount, "Data Structures and Algorithms in C++", Wiley, Second Edition, 2011.
- Kruse, Leung, Tondo, "Data Structures and Program Design in C", Pearson Education, Second Edition, 2013.
- Seymour Lipschutz, "Data Structures", Schaum's Outline Series, Tata McGraw-Hill, First Edition, 2014.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of the marks scored in both the tests will be considered for final grading.



Data Structures & Algorithms Laboratory (22PCET5050L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01 Examination Scheme

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

- 1. Understand and remember algorithms and its analysis procedure.
- 2. Introduce the concept of data structures through ADT including List, Stack, Queues.
- 3. To design and implement various data structure algorithms.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Design and implement various data structures such as arrays, linked lists, stacks, queues and trees	L2	Comprehension
CO2	Understand the operations of data structures	L4	Analyze
CO3	Determine and analyze the complexity of given Algorithms.	L4, L5	Analyze, Syn- thesize



List of Laboratory Experiments: (Any Seven)

- 1. To implement stack.
- 2. To implement parenthesis checking using stack
- 3. Implementation of Infix to Postfix conversion
- 4. To implement Implementation of prefix and postfix evaluation using menu driven approach.
- 5. To implement Linear queue.
- 6. To implement Circular queue.
- 7. To implement different operations on linked list copy, concatenate, split, reverse, count no. of nodes
- 8. To implement various operations on doubly linked list
- 9. To implement Stack using Linked List
- 10. To implement Queue using Linked List
- 11. To create a binary tree and traverse it in Inorder, preorder and Postorder
- 12. To implement binary search tree

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Laboratory work (Performance in Experiments): 15 Marks
- 2. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination will be based on the entire syllabus including, the practicals performed during laboratory sessions.

Practical Scheme

Practical : 02 Hrs./week Credit : 01

Examination Scheme

Teacher Assessment : 25 Marks End Sem Exam : 25 Marks Total : 50 Marks

Pre-requisite:

- 1. Electronics Circuit Design
- 2. Digital System Design
- 3. Integrated Circuit
- 4. Python Programming Laboratory

- To determine the goals, resource requirements of project and produce them in the form of documentation.
- To learn effective utilization of time and project management skills.
- To address the real-world projects, to connect theory with practice as per recent industrial trends.
- To integrate knowledge and skills from various areas through more complex and multidisciplinary projects.

СО	Course Outcomes	Blooms Level	Blooms Description
CO1	Define problem statement, formulation and solution by reviewing relevant literature	L4	Analyze
CO2	Identify alternate approaches to complete a project	L2	Understand
CO3	Apply project management skills by interacting and dividing project work among team member	L3	Apply
CO4	Develop technical, communication, and presentation skills	L3	Apply



Syllabus:

Domain knowledge (any beyond) needed from the following areas for the effective implementation of the project:

1. Microcontroller and Embedded Systems, Signal Processing, Microwave and Antennas, Networking and Internet of Things, Data science and Big data, Communication, Web and Application development, Robotics, AI and Machine learning.

The above areas can be updated based on the technological innovations and development needed for specific project.

Guidelines:

The main purpose of this activity is to improve the students' documentation and technical skills to find the cost effective solution. Guidelines are as follows:

- The project work is to be carried out by a group of 4/5/6 students(2/3 second year and 2/3 third year students)
- Each group is allotted a final year student as a mentor and a faculty member as a guide.
- Project topics will be floated in various domains. Each group submits three project topic preferences, out of which one topic is allotted in discussion with faculty guide and faculty coordinator.
- Each group will identify the hardware and software requirement for their problem statement.
- Each group will be reviewed twice in a semester (August and October) and marks will be allotted based on the various points mentioned in the evaluation scheme.
- In the first review of this semester, each group is expected to complete the literature survey, documentation and budgeting of the project.
- In the second review of this semester, each group is expected to complete 30 % of project.
- Subsequent reviews will be carried out in sixth semester.

Assessment Criteria:

- At the end of the semester, after confirmation by the project guide, each project group will submit project completion report in prescribed format for assessment to the departmental committee (including project guide).
- Assessment of the project (at the end of the semester) will be done by the departmental committee (including project guide).

• Oral examination should be conducted by Internal and External examiners. Students have to give presentation and demonstration based on their project.

Prescribed project report guidelines:

Size of report shall be of minimum 25 pages. Project Report should include appropriate content for:

- Introduction
- Literature Survey
- Related Theory
- Implementation details
- Project Outcomes
- Conclusion
- References

Assessment criteria for the departmental committee (including project guide) for Continuous Assessment:

Guide will monitor weekly progress and marks allocation will be as per Table 2.

Assessment criteria for the departmental committee (including project guide) for End Semester Exam:

Departmental committee (including project guide) will evaluate project as per Table 3. Each group shall present/publish a paper based on the semester project in reputed/peer reviewed

Conference/Journal/TechFest/Magazine/ before the end of the semester.

Each group will be reviewed twice in a semester by faculty guide and faculty coordinator based on the following criteria:

- Objective and Expected outcome
- Long term social impact
- Innovativeness and Motivation
- Documentation
- Simulation effectiveness
- Literature survey and Comparative Methodology
- Project Progress/Implementation
- Overall Presentation and Team work


Each review consists of 25 marks. Average of the marks scored in both the two reviews will be considered for final grading. The final certification and acceptance of TW ensures the satisfactory performance on the above aspects.

Sr	Week (Start Date:End Date)	Work Done	Sign of Guide	Sign of Coordinator
1				
2				

Table 2: Continuous Assessment Sheet

Sr	Exam Seat No	Name of Student	Student Attendance	Log Book Maintain	Literature Review	Depth of Un- derstanding	Report	Total
			5	5	5	5	5	25

Table 3: Evaluation Sheet

Sr	Exam Seat No	Name of Student	Project Selection	Design/ Simulation/ Logic	PCB/ hardware/ program- ming	Result Ver- ification	Presentation	Total
			5	5	5	5	5	25



Employability Skill Development Program - II (22HMET5070L)

Teaching Scheme

Examination Scheme

Practical: 02 Hrs/Week Credit: 01 Teacher Assessment: 50 Marks Teacher Total: 50 Marks

Pre-requisite:

1. Basic Mathematics

2. Basic knowledge of C programming

- 1. To enhance the problem solving skills with real life examples.
- 2. To enable the students to express their thoughts and knowledge on various platforms.
- 3. Able to describe the basic database management system.
- 4. Able to implement basic programming project using python.

COs	Course Outcomes	Blooms Level	Blooms Description
CO1	Analyze and solve the logical problem based on words, venn dia-	L2 and	Understand,
	gram etc.	L3	Apply
CO2	Understand and solve the English comprehension, Sentence com-	L2 and	Understand,
	pletion, Sentence Correction problems.	L3	Apply
CO3	Understand and illustrate the concept of Exception Handling,	L2 and	Understand,
	Garbage collection	L3	Apply
CO4	Understand and describe the fundamental of DBMS, NoSql, Mon-	L2 and	Understand,
	goDB.	L3	Apply



Unit-I

Reasoning : Data sufficiency, Logical Deductions, Logical Sequence of Words, Logical Venn Diagrams,
Statement and Arguments, Statement and Assumptions, Statement and Conclusions Syllogism.
English: Reading Comprehension, Para Jumbles, Cloze Test, Tenses/ Voice/ Speech, Prepositions/
SVA/ Articles, Vocab /Verbal Analogy, Sentence completion, Sentence Correction.

Unit-II

Modules: Introduction, Importance of Modularity programming, Import keyword, User defined modules creation, Function based modules, Classes based modules, Connecting modules, 'from' keyword. **Files Handling:** Reading file char by character, Reading file line by line, Modes of files, Writing into file, Append data to a file, Reading CSV file, Pickling and Un pickling.

Garbage collection: Introduction, Importance of manual GC, Self-referenced objects, 'gc' module, Collect() method, Threshold function.

Unit-III

Collections Framework: Introduction to collection of data types, Importance of Data processing, DS algorithms introduction.

List: Create a list, Adding elements, Deleting elements, Pre-defined functionality of List, Nested List, Immutability and Mutability of List.

Set: The functionality of Set object, Frozen set, Dictionaries, Create a dictionary, Adding elements **Dict:** Pre-defined functions of Dict class, Programs using Collection types

Unit-IV

Tkinter GUI Types of Layouts , Create Labels and Display images, Create Buttons, Create Events, StringVar class, Calculator program using GUI.

Basic ML AI including Projects Iterators, Nested functions, Generators, Closures, Decorators, Basic ML and AI, PIP, Visualization etc. Project Domain (Per domain 1 or 2 project)

ML/AI Based Projects

Data Analysis Based projects

Test Summarization based projects

web scrapping and crawling

Unit-V

10 Hrs.

DBMS Using Python: Introduction to Mysql, Mysql Python connectivity, DDL, DRL, DML, Transaction management examples (rollback and commit), GUI Database connectivity.

10 Hrs.

10 Hrs.

10 Hrs.

10 Hrs.

No Sql Using Python: Installation and Configuration, MongoDB Tools, Collection and Documents, CRUD and the MongoDB Shell, Introduction to CRUD, Introduction to the MongoDB API, Creating a Database, Collection and Documents.

Data Modelling and Schema Design: MongoDB Database References Model Tree Structures, MongoDB Analysing Queries, Atomic Operations, Map Reduce, Text Search, Regular Expression, Capped Collections.

Administration: MongoDB Deployment and Cluster setup, MongoDB GridFS, Trident Spout, Working with Replica Sets, MongoDB Sharding.

Reference Books

- 1. Dr. R S Aggarwal, Quantitative Aptitude for Competitive Examinations, S. Chand Publication.
- 2. M. G. Venkateshmurthy, Programming Techniques through C, Pearson Publication.
- 3. Behrouz Forouzan, A Computer Science Structure Programming Approaches using C, Cengage Learning.
- 4. Yashwant Kanetkar, Let Us C, BPB Publication.

Evaluation Scheme:

Continuous Assessment (CA)

Teacher's assessment (TA) will carry weightage of 50 marks. Components of TA are:

- 1. MCQ Test based on Aptitude: 20 Marks
- 2. MCQ Test based on Programming skills: 20 Marks.
- 3. Mock Interview: 10 Marks

Any other component recommended by BOS and approved by Dean Academics.



Environmental Engineering (22MCET5080T)

Teaching Scheme	Audit Course
Lectures: 01 Hrs./Week	

- 1. Understand environmental issues such as depleting resources, pollution, ecological problems and the renewable energy scenario.
- 2. Familiarise environment related legislation

COs	Course Outcomes	Blooms Level	Blooms Description
CO1	Understand how human activities affect environment.	L1	Remember
CO2	Understand the various technology options that can make a dif- ference.	L1	Remember



Unit-I Social Issues and Environment 04 Hrs.

Ecological footprint and Carrying Capacity, Depleting nature of Environmental resources such as soil, water minerals and forests ,Carbon emissions and Global Warming.

Unit-II Technological growth for Sustainable Development 04 Hrs.

Technological growth for Sustainable Development: Social, Economic and Environmental aspects of Sustainable Development, Renewable Energy Harvesting ,Concept of Carbon credit, Green Building ,Power and functions of Central Pollution Control Board and State Pollution Control Board.

Unit-III Environmental impact due to technology 05 Hrs.

Environmental impact due to technology: Impact of Energy on Environment, Flow of Energy in Ecological system, Environment Degradation due to Energy, Control of pollution from Energy, Consumer electronics, power saving devices, energy from waste, energy use and conservation.

Text Books

- 1. Environmental Studies From Crisis to Cure, R. Rajagopalan, 2012.
- 2. Textbook for Environmental Studies For Undergraduate Courses of all Branches of Higher Education, Erach Bharucha.
- 3. Environmental Management Science and Engineering for industry by Iyyanki V. Murlikrishna and valli Manickam.

Evaluation Scheme:

- 1. Student should submit a report on the case study declared by teacher.
- 2. Audit point shall be awarded subject to submission of report of the case study declared by teacher.





Shirpur Education Society's

R. C. Patel Institute of Technology, Shirpur (An Autonomous Institute)

Syllabus Booklet Electronics and Telecommunication Engineering

Third Year B. Tech. Semester-VI

With Effect from Academic Year 2024-25





Shahada Road, Near Nimzari Naka, Shirpur, Maharashtra 425405 Ph: 02563 259 802, Web: www.rcpit.ac.in

Teaching Scheme Lectures: 03 Hrs./Week Credit: 03

Examination Scheme

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite:

- 1. Signal and System
- 2. Engineering Mathematics IV
- 3. Analog Communication

- 1. To learn about theoretical bounds on the rates of digital communication system and represent a digital signal using several modulation methods.
- 2. To draw signal space diagrams, compute spectra of modulated signals and apply redundancy for reliable communication.

COs	Course Outcomes	Blooms Level	Blooms Description
CO1	Apply the basics of information theory and coding techniques to determine the minimum number of bits per symbol required to represent the source and the maximum rate at which a reliable communication can take place over the channel.	L3	Apply
CO2	Describe and determine the performance of different waveform techniques for the generation of digital representation of signals	L6	Evaluate
CO3	Determine methods to mitigate inter symbol interference in base- band transmission system	L6	Evaluate
CO4	Describe and determine the performance of different error control coding schemes for the reliable transmission of digital representa- tion of signals and information over the channel of Communication systems.	L6	Evaluate



Unit-I Information theory and source coding 09 Hrs.

Block diagram and sub-system description of a digital communication system, measure of information and properties, entropy and it's properties, Source Coding, Shannon's Source Coding Theorem, Shannon- Fano Source Coding, Huffman Source Coding, Differential Entropy, joint and conditional entropy, mutual information and channel capacity, channel coding theorem, channel capacity theorem.

Unit-II Baseband Modulation and Transmission 06 Hrs.

Discrete PAM signals and its power spectra, Inter-symbol interference, correlative coding, equalizers, and eye pattern.

Unit-III Band pass Modulation and Demodulation 12 Hrs.

Band pass digital transmitter and receiver model, digital modulation schemes Generation, detection, signal space diagram, spectrum, bandwidth efficiency, and probability of error analysis of: Amplitude Shift Keying (ASK), Frequency Shift Keying (FSK)Modulations, Binary Phase Shift Keying (BPSK) Modulation, Quaternary Phase Shift Keying (QPSK), Quadrature Amplitude Modulation (QAM), Comparison between bandwidth and bit rate, applications of digital modulation schemes

Unit-IV Error Control Systems 13 Hrs.

Types of error control, error control codes Linear Block Codes: vector spaces, vector sub spaces, generator matrix, systematic linear block codes, parity check matrix, syndrome testing, error correction, and decoder implementation Cyclic codes: Algebraic structure of cyclic codes, binary cyclic code properties, encoding in systematic form, circuits for dividing polynomials, systematic encoding with shift register and error detection Convolution Codes: Time domain and transform domain approach, graphical representation, code tree, trellis, state diagram, decoding methods, maximum likelihood decoding, and free distance.

Text Books

- Haykin Simon, "Digital Communication Systems," John Wiley and Sons, New Delhi, Fourth Edition, 2014.
- H. Taub, D. Schlling, and G. Saha, "Principles of Communication Systems," Tata Mc-Graw Hill, New Delhi, Third Edition, 2012.

Reference Books

 Sklar B, and Ray P. K., Digital Communication: Fundamentals and applications, Pearson, Dorling Kindersley (India), Delhi, Second Edition, 2009.

- T L Singal, Analog and Digital Communication, Tata Mc-Graw Hill, New Delhi, First Edition, 2012.
- P Ramakrishna Rao, Digital Communication, Tata Mc-Graw Hill, New Delhi, First Edition, 2011.
- M F Mesiya, Contempory Communication systems, Mc-Graw Hill, Singapore, First Edition, 2013.
- 5. Lathi B P, and Ding Z., Modern Digital and Analog Communication Systems, Oxford University Press, Fourth Edition, 2009.

Evaluation Scheme:

Continuous Assessment (CA)-Theory:

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of both the two tests will be considered for final grading.
- 4. Teacher's assessment (TA) will carry weightage of 20 marks.Components of TA are Individual Presentation (Mandatory), Group Discussion (Mandatory, Quizzes, Class Tests/ Surprise Tests/ Open Book Tests, research Paper Presentation, Viva, Any other component recommended by BOS and approved by Dean Academics.

End Semester Examination (ESE)

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.

Digital Communication Laboratory (22PCET6010L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01 Examination Scheme

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

Pre-requisite:

- 1. Signal and System
- 2. Engineering Mathematics IV
- 3. Analog Communication

- 1. To learn about theoretical bounds on the rates of digital communication system and represent a digital signal using several modulation methods.
- 2. To draw signal space diagrams, compute spectra of modulated signals and apply redundancy for reliable communication.

COs	Course Outcomes	Blooms Level	Blooms Description
CO1	Apply the basics of information theory and coding techniques to determine the minimum number of bits per symbol required to represent the source and the maximum rate at which a reliable communication can take place over the channel.	L3	Apply
CO2	Describe and determine the performance of different waveform techniques for the generation of digital representation of signals	L6	Evaluate
CO3	Determine methods to mitigate inter symbol interference in base- band transmission system	L6	Evaluate
CO4	Describe and determine the performance of different error control coding schemes for the reliable transmission of digital representa- tion of signals and information over the channel of Communication systems.	L6	Evaluate

List of Laboratory Experiments: (Any Eight)

- 1. Entropy and Mutual Information
- 2. Source Coding Algorithms(Huffman coding)
- 3. Linear block codes(Error detection and correction)
- 4. Cyclic codes(comparison of performance of coded and uncoded system)
- 5. Convolutional Encoding Time domain approach
- 6. ASK, FSK And PSK
- 7. Generation and Detection of Binary Amplitude shift keying(BASK)
- 8. Generation of Binary FSK signal modulation (FSK)
- 9. Observing Eye pattern

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination of 25 Marks will be based on the entire syllabus including, the practicals performed during laboratory sessions.

Teaching Scheme Lectures: 03 Hrs./Week Credit: 03

Examination Scheme

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite:

1. Radio Frequency Circuit Design

- 1. To learn fundamental parameters of Antenna.
- 2. To learn about linear wire antenna elements and Antenna arrays.
- 3. To learn about Special types of Antennas.
- 4. To learn measurement procedures of Antenna parameters.

COs	Course Outcomes	Blooms Level	Blooms Description
CO1	Explain and measure basic antenna parameters like radiation pat- tern, input impedance, gain and polarization.	L1	Understand
CO2	Derive the field equations for the basic radiating elements like linear wire antenna and loop antenna.	L4	Analysis
CO3	Design of uniform linear and planar antenna arrays using isotropic and directional Sources.	L5	Synthesis
CO4	Design regular shape microstrip antennas and aperture antennas.	L5	Synthesis

Unit-I Antenna Fundamentals 10 Hrs.

Review of Maxwell's equations and vector potential wave equation. Antenna Parameters: Near field and far field radiation, dual equations for electric and magnetic current sources, radiation Mechanism, basic antenna parameters, Radiation pattern, radiation power density, radiation intensity, beam width, directivity, Antenna efficiency, Gain, beam efficiency, bandwidth, polarization, input impedance, antenna vector effective length and equivalent areas, antenna radiation efficiency, FRIIS transmission equation. Measurement of Antenna parameters: Input Impedance, Radiation Pattern, Gain (Two and Three antenna, method), polarization.

Unit-II Wire Elements: Dipoles, Monopoles, Loops and Helical 09 Hrs.

Infinitesimal dipole, radiation fields, radiation resistance, radiation sphere, near field, far field directivity, small dipole, finite length dipole, half wave length dipole, linear elements near or on infinite perfect conductors, Monopole antenna, Folded dipole. Design of dipole and monopole antenna. Loop Antenna: Small circular loop, comparison of small loop with short dipole, Ferrite loop, Radiation patterns, its parameters, and their applications. Helical Antennas: Input impedance matching, Axial mode and normal mode propagation, Circular polarization using Helical Antenna.

Unit-III Arrays 09 Hrs.

Linear arrays, Array of two isotropic point sources, linear arrays of N elements, principle of pattern multiplication applicable to non-isotropic sources, Phase scanning arrays, Broadside and End-fire Array, Increased Directivity end fire array, Calculations of Directivity, Beam width, Maxima and null directions for N-element Array, basics of planar arrays. Design of Yagi antenna and Log Periodic antenna.

Unit-IV Microstrip Antenna 06 Hrs.

Microstrip antenna (MSA): Introduction, Feeding Techniques, Regular Shape MSAs (Rectangular, Circular, Equilateral Triangular), Design of Regular shape MSAs.

Unit-V Aperture Antennas 06 Hrs.

Horn Antennas: E-Plane Sectoral Horn, H-Plane Sectoral Horn, Pyramidal Horn, Conical Horn. Reflector Antennas: Introduction, Plane Reflector, Corner Reflector, Parabolic Reflector, Design considerations.

Text Books

- 1. C. A. Balanis, Antenna Theory Analysis and Design, John Wiley Sons, Third Edition, 2016.
- 2. G. Kumar, K. P. Ray, Broadband Microstrip Antenna, Artech House, First Edition, 2002.

Reference Books

 R. E. Collin, Antennas and Radio Wave Propagation, International Student Edition, McGraw Hill, Fourth Edition, 1985.

Evaluation Scheme:

Continuous Assessment (CA)-Theory:

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of both the two tests will be considered for final grading.
- 4. Teacher's assessment (TA) will carry weightage of 20 marks.Components of TA are Individual Presentation (Mandatory), Group Discussion (Mandatory, Quizzes, Class Tests/ Surprise Tests/ Open Book Tests, research Paper Presentation, Viva, Any other component recommended by BOS and approved by Dean Academics.

End Semester Examination (ESE)

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.

Radiating Systems Laboratory (22PCET6020L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01

Examination Scheme

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

Pre-requisite:

1. Radio Frequency Circuit Design

- 1. To learn fundamental parameters of Antenna.
- 2. To learn about linear wire antenna elements and Antenna arrays.
- 3. To learn about Special types of Antennas.
- 4. To learn measurement procedures of Antenna parameters.

COs	Course Outcomes	Blooms Level	Blooms Description
CO1	Explain and measure basic antenna parameters like radiation pat- tern, input impedance, gain and polarization.	L1	Understand
CO2	Derive the field equations for the basic radiating elements like linear wire antenna and loop antenna.	L4	Analysis
CO3	Design of uniform linear and planar antenna arrays using isotropic and directional Sources.	L5	Synthesis
CO4	Design regular shape microstrip antennas and aperture antennas.	L5	Synthesis

List of Laboratory Experiments: (Minimum Eight)

- 1. Study of Antenna types
- 2. Plot Radiation Pattern of dipole and monopole using Antenna trainer kit/ simulation software
- 3. Plot Radiation Pattern of dipole for varying length using simulation software
- 4. Design of RMSA using simulation software
- 5. Design of CMSA using simulation software
- 6. Design of ETMSA using simulation software
- 7. Plot Radiation Patterns of microstrip antenna using Antenna trainer kit
- 8. Design of Broad side-end fire array
- 9. Study of pattern multiplication
- 10. Design of phase scanning array.
- 11. Gain measurement using three antenna method.
- 12. Radiation pattern measurement.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination of 25 Marks will be based on the entire syllabus including, the practicals performed during laboratory sessions.

Teaching Scheme Lectures: 03 Hrs./Week Credit: 03

Examination Scheme

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite:Knowledge of

1. Analog Communication

- 1. To Learn various hardware network components.
- 2. To understand network reference models and process involved in data communication.
- 3. To understand the protocols working at different layers.
- 4. To design and configure a network for an organization.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Compare OSI layered architecture with TCP/IP protocol suite and differentiate functions of each layer.	L2	Comparing
CO2	Define characteristics of physical media and differentiate among multiplexing techniques	L1	Understand
CO3	Understand responsibilities of the data link layer and explain the datalink layer protocols.	L1	Understand
CO4	Design network and subnetwork and list the commands required to carry out investigations and troubleshooting.	L5	Evaluate
CO5	Distinguish transport layer protocols based on application.	L2	Comparing

Unit-I Introduction to computer network 06 Hrs.

Reference Models, OSI model, overview of TCP/IP, layer functions, services, peer to peer protocols, sockets and ports, Data encapsulation, Networking devices: Repeater, hub, bridge, switch and routers, Network topology.

Unit-II Introduction to Physical layer Services 04 Hrs.

Introduction to physical media, Coax, RJ 45, Optical fiber, twisted pair, bit transmission, frequency division multiplexing. Time division multiplexing.

Unit-IIIThe Data Link Layer10 Hrs.

Data link Layer Design Issues, Error Detection and Correction, Elementary Data Link Protocols, Sliding Window Protocols: Stop and Wait protocol, Go-back-n protocol, Selective-repeat protocol, Example Data Link Protocols: HDLC: High-Level Data Link Control, The Data Link Layer in The Internet, Channel Allocation Problem, Multiple Access Protocols.

Unit-IV The Network Layer 10 Hrs.

Network functions for the Network Layer Functions, Routing Algorithms: Distance vector and Link state routing, shortest path first algorithm: Dijikstra and Bellman Ford algorithm, Quality of Service. Network Layer In The Internet: The IP Protocol, IPv4 header, IP Addressing classfull and classless, CIDR notation, Subnetting, suppernetting, Internet Control Protocols, The Interior Gateway Routing Protocol: RIP, OSPF, and The Exterior Gateway Routing Protocol: BGP.

Unit-V The Transport Layer 10 Hrs.

The Transport Service, Elements of Transport Protocols, The Internet Transport Protocol: TCP and UDP, The Internet Transport Protocol: TCP:-Introduction to TCP, The TCP, Service Model, The TCP Protocol, The TCP Segment Header, TCP Connection Establishment, TCP Connection Release, Modelling TCP Connection Management, TCP Transmission Policy, TCP Congestion Control, TCP Timer Management.

Text Books

- 1. A. S. Tanenbaum, Computer Network, 4th edition, Prentice Hall
- 2. B. F. Ferouzan, Data and Computer Communication, Tata McGrawHill.

Reference Books

1. Kurose, Ross, Computer Networking, Addison Wesley

- 2. W. Richard Stevens, TCP/IP Volume1, 2, 3, Addison Wesley.
- 3. D.E.Comer, Computer Networks And Internets, Prentice Hall.
- 4. B. F.Ferouzan , TCP/IP Protocol Suite, Tata Mc-Graw Hill.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.

Computer Networks Laboratory (22PCET6030L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01

Examination Scheme

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

Pre-requisite:Knowledge of

1. Analog Communication

- 1. To Learn various hardware network components.
- 2. To understand network reference models and process involved in data communication.
- 3. To understand the protocols working at different layers.
- 4. To design and configure a network for an organization.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Compare OSI layered architecture with TCP/IP protocol suite and differentiate functions of each layer.	L2	Comparing
CO2	Define characteristics of physical media and differentiate among multiplexing techniques	L1	Understand
CO3	Understand responsibilities of the data link layer and explain the datalink layer protocols.	L1	Understand
CO4	Design network and subnetwork and list the commands required to carry out investigations and troubleshooting.	L5	Evaluate
CO5	Distinguish transport layer protocols based on application.	L2	Comparing

List of Laboratory Experiments: (Any Eight)

- 1. To implement different networking command using Cisco packet tracer.
- 2. To study various hardware and software network components.
- 3. To configure the Web (HTTP and DNS), FTP and SMTP server using cisco packet tracer
- 4. To configure RIP protocol in a network using Cisco packet tracer.
- 5. To configure OSPF protocol in a network using Cisco packet tracer.
- 6. To establish TELNET session using Cisco packet tracer.
- 7. To design Firewall using standard and extended ACLs.
- 8. To study VLSM using Cisco packet tracer
- 9. To implement Dijikstra's algorithm
- 10. To implement Bellman Ford algorithm
- 11. To analyze network traffic: HTTP, TCP, UDP using Wireshark

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination of 25 Marks will be based on the entire syllabus including, the practicals performed during laboratory sessions.

Fundamentals of Digital Image Processing (22PCET6040T)

Teaching Scheme

Lectures: 03 Hrs./Week Credit: 03 Examination Scheme

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite:Knowledge of

- 1. Engineering Mathematics-IV
- 2. Digital Signal Processing

Course Objectives

1. To cover the fundamentals, mathematical models and transformation techniques in digital image processing.

COs	Course Outcomes	Blooms Level	Blooms scription	De-
CO1	Understand and analyze concept of sampling, quantization and various color models in image processing.	L3	Apply	
CO2	Implement various image enhancement algorithms in spatial do- main.	L3	Apply	
CO3	Apply different image Transforms in applications.	L3	Apply	
CO4	Apply various filters for image restoration.	L4	Analyze	
CO5	Recognize different shapes using various representa- tion/segmentation techniques and classify the object using different classification methods.	L5	Evaluate	

2. To develop time and frequency domain techniques for image enhancement.

Unit-I Digital Image Fundamentals 04 Hrs.

Steps in Digital Image Processing, Components, Image Sampling and Quantization Color Image Processing: Color Fundamentals Color models.

Unit-II Image Enhancement (point processing) 12 Hrs.

Image Negative, Thresholding, Gray level slicing with and without background, power law and log transform, Contrast Stretching, Histogram equalization and Histogram Specification Image Enhancement in Spatial Domain (Neighborhood processing): Basics of Spatial Filtering, Generating Spatial Filter MasksSmoothing and Sharpening Spatial Filtering Image Transforms: 1-D DFT, 2-D Discrete Fourier Transform and Its Inverse, Some Properties of 2D DFT, Walsh -Hadamard, Discrete Cosine Transform, Haar Transform, Slant Transform Image Enhancement in Frequency Domain: The Basics of Filtering in the Frequency Domain, Smoothing and Sharpening frequency domain filters.

Unit-III Morphology 06 Hrs.

Erosion and Dilation, Opening and Closing, The Hit or-Miss Transformation. Restoration: Noise models Mean Filters Order Statistics Adaptive filters wiener filter.

Unit-IV Point, Line, and Edge Detection 12 Hrs.

Detection of Isolated Points, Line detection, edge models, basic and advance edge detection, Edge linking and boundary detection, Canny's edge detection algorithm Thresholding: Foundation, Role of illumination, Basic Global thresholding, Otsu's method Region Based segmentation: Region Growing, Region Splitting and merging, Relationships between pixels, Hough transform Region Identification: chain code, simple geometric border representation, Fourier Transform of boundaries, Boundary description using segment sequences.

Unit-V Object Recognition 08 Hrs.

Knowledge representation, Classification Principles, Classifier setting, Classifier Learning, Support vector machine, Kernels, cluster analysis, K means Clustering.

Text Books

- 1. Gonzales and Woods, Digital Image Processing, Pearson Education, India, 3rd Edition,
- Milan Sonka, Vaclav Hlavac, Roger Boyle, Image Processing, Analysis, and Machine Vision, Cengage Engineering, 3rd Edition, 2013

Reference Books

- 1. Anil K.Jain, Fundamentals of Image Processing, Prentice Hall of India, First Edition, 1989.
- 2. W Pratt, Digital Image Processing, Wiley Publication, 3rd Edition, 2002

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.

Fundamentals of Digital Image Processing Laboratory (22PCET6040L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01

Examination Scheme

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

Pre-requisite:Knowledge of

- 1. Engineering Mathematics-IV
- 2. Digital Signal Processing

Course Objectives

1. To cover the fundamentals, mathematical models and transformation techniques in digital image processing.

COs	Course Outcomes	Blooms Level	Blooms scription	De-
CO1	Understand and analyze concept of sampling, quantization and various color models in image processing.	L3	Apply	
CO2	Implement various image enhancement algorithms in spatial do- main.	L3	Apply	
CO3	Apply different image Transforms in applications.	L3	Apply	
CO4	Apply various filters for image restoration.	L4	Analyze	
CO5	Recognize different shapes using various representa- tion/segmentation techniques and classify the object using different classification methods.	L5	Evaluate	

2. To develop time and frequency domain techniques for image enhancement.

List of Laboratory Experiments: (Minimum Eight)

- 1. To perform basic Image Processing, Geometric, Arithmetic and Logical operations on images.
- 2. To perform Spatial Domain Image Enhancement using different Point Processing techniques.
- 3. To perform Spatial Domain Image Enhancement using different Neighborhood Processing techniques.
- 4. To perform Histogram equalization.
- 5. Application of Harr transform in image processing.
- 6. To perform frequency domain Image Enhancement techniques.
- 7. To perform region-based segmentation.
- 8. To perform morphological operations on Image.
- 9. To perform edge detection using basic and advanced techniques.
- 10. To perform Image restoration using various filters.
- 11. To perform classification using Support Vector Machine.
- 12. To perform clustering using K-means algorithm.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination of 25 Marks will be based on the entire syllabus including, the practicals performed during laboratory sessions.

Basic VLSI (22PEET6051T)

Teaching Scheme

Examination Scheme

Lectures: 03 Hrs./Week Credit: 03 Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite:Knowledge of

- 1. Electronics Circuit Design
- 2. Digital System Design
- 3. Integrated Circuits

- 1. To highlight the circuit design issues in the context of VLSI technology.
- 2. To provide understanding of VLSI circuit design using different design styles.
- 3. To provide introduction to HDL programming.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Understand transistor scaling and VLSI circuit performance.	L1	Understand
CO2	Realize logic circuits using different design styles.	L4	Analyze
CO3	Understand operation of memory, storage circuits and data path elements.	L1	Understand
CO4	Design digital circuits using HDL language.	L4 L5	Analyze , Syn- thesize

Unit-I MOSFET Scaling 06 Hrs.

Types of scaling, short channel effects. Layout: Lambda based design rules (CMOS), MOSFET capacitances. .

Unit-II CMOS INVERTER Circuit Analysis 14 Hrs.

Static and dynamic analysis (Noise, propagation delay and power dissipation) of resistive load and CMOS inverter. Comparison of all types of MOS inverters. Design of CMOS inverters and its layout. Design styles: Static CMOS, Dynamic CMOS, pass transistor logic, transmission gate, Pseudo NMOS, Domino logic, C2MOS, NORA logic, NP Domino logic ,Realization of Multiplexer (up to 4:1 Mux) , Encoder, Decoder, SR Latch, JK FF, D FF, 1 Bit Shift Register design in different design styles and their layouts.

Unit-III Memory and Storage circuits 08 Hrs.

ROM array, SRAM (operation, design strategy, leakage currents, read /write circuits), layout of SRAM. DRAM (Operation of 1T, 3T, operation modes, refresh operation, Input-Output circuits), layout of DRAM.

Unit-IV Data path design 08 Hrs.

Full adder, Ripple carry adder, CLA adder, Carry Skip Adder, Carry Save Adder and carry select adder, Array Multiplier, Barrel shifter.

Unit-V Design methods 04 Hrs.

Semi-custom Full custom design PLA PAL PROM FPGA PLD. Introduction to Verilog Programming.

Text Books

- Sung-Mo Kang and Yusuf Leblebici, CMOS Digital Integrated Circuits Analysis and Design, Tata McGraw Hill, Third Edition, 2012.
- Samir Palnitkar, Verilog HDL: A Guide to Digital Design and Synthesis, PHI, Second Edition, 2017.

Reference Books

- Jan M. Rabaey, Anantha Chandrakasan and Borivoje Nikolic, Digital Integrated Circuits: A Design Perspective, Pearson Education, Second Edition, 2003.
- JP. Uyemura, Introduction to VLSI Circuits and Systems, John Wiley Sons, First Edition, 2006.

- Frank Vahid, Digital Design with RTL design, VHDL and VERILOG, John Wiley and Sons Publisher, Second Edition, 2011.
- 4. Neil H. E. Weste, David Harris and Ayan Banerjee, CMOS VLSI Design: A Circuits and Systems Perspective, Pearson Education, 3rd Edition, 2006.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.

Basic VLSI Laboratory (22PEET6051L)

Teaching Scheme Practical: 02 Hrs/Week Credit: 01

Examination Scheme

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

Pre-requisite:Knowledge of

- 1. Electronics Circuit Design
- 2. Digital System Design
- 3. Integrated Circuits

- 1. To highlight the circuit design issues in the context of VLSI technology.
- 2. To provide understanding of VLSI circuit design using different design styles.
- 3. To provide introduction to HDL programming.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Understand transistor scaling and VLSI circuit performance.	L1	Understand
CO2	Realize logic circuits using different design styles.	L4	Analyze
CO3	Understand operation of memory, storage circuits and data path elements.	L1	Understand
CO4	Design digital circuits using HDL language.	L4 L5	Analyze , Syn- thesize

List of Laboratory Experiments: (Any Eight)

- 1. To study MOS characterization using simulation software. bias).
- 2. Static analysis of CMOS Inverter.
- 3. Dynamic analysis of CMOS Inverter
- 4. Multiplexer design using pass transistor and transmission gate logic style.
- 5. 1-bit CMOS Adder design using static CMOS logic style.
- 6. 1-bit CMOS mirror Adder design.
- 7. To write Verilog Program for flip flops.
- 8. To write Verilog Program for adders.
- 9. To write Verilog Program for multiplexers.
- 10. To design of Wilson current mirror.
- 11. Design and simulation of barrel shifter circuit in SPICE.
- 12. To write Verilog code and simulation of barrel shifter.
- 13. To study MOS characterization using simulation software.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination of 25 Marks will be based on the entire syllabus including, the practicals performed during laboratory sessions.

Teaching Scheme

Examination Scheme

Lectures: 03 Hrs./Week Credit: 03 Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite:Knowledge of

- 1. Basic Electrical Engineering & Digital Electronics
- 2. Digital System Design
- 3. Engineering Mathematics-I
- 4. Engineering Mathematics-II

- 1. To provide fundamental concept of control systems.
- 2. To introduce mathematical modelling, time domain analysis frequency domain analysis.
- 3. To develop concepts of stability and its assessment criteria of the system.
- 4. To study basic concepts of controllers.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Understand the basic concepts of control system.	L1	Understand
CO2	Derive the mathematical model of different type of the systems.	L4	Analyze
CO3	Analysis of systems in time and frequency domain.	L4, L5	Analyze, Eval- uate
CO4	Understand & Find stability of given system using appropriate criteria.	L1	Understand
CO5	Apply the control theory to design the conventional controllers widely used in the industries.	L	Apply

Unit-I Introduction to Control Systems 08 Hrs.

Introduction: Open loop, closed loop systems, feed forward control, adaptive control systems, examples of control systems. Modeling: Types of models, impulse response model, transfer function model. Dynamic Response: Standard test signals, transient and steady state behavior control systems, steady state errors in feedback control systems and their types.

Unit-II Mathematical Modeling of Systems 10 Hrs.

Conversion of block diagram to signal Flow Graph and Vice-versa., Transfer Function models of various Electrical systems, Block diagram reduction for single inputs single outputs (SISO) and multiple inputs multiple outputs(MIMO) systems, signal flow graph, Masons gain rule.

Unit-III State Variable Models 07 Hrs.

Basic concepts, state variable and state models for electrical systems, general state space representation, conversion between state space and transfer function, concept of state transition matrix, properties of state transition matrix, controllability and observability, analysis of LTI systems, with Examples.

Unit-IV Stability Analysis 10 Hrs.

Concept of stability, Routh stability criterion, Root-locus, general rules for constructing root-locus, Magnitude and phase plot; Method of plotting Bode plot; Stability margins on the Bode plots, Nyquist stability criterions gain and phase margins. Case study on stability of Control System in Thermal Power Plant.

Unit-V Controllers Compensators 05 Hrs.

Introduction of PI, PD, and PID Controllers, Lead and Lag compensators. Case study on a modeldriven PID control system.

Text Books

- I. J. Nagrath, Madan.Gopal, Control System Engineering, New Age International Publication, Seventh Edition, 2021.
- 2. K.Ogata, Modern Control Engineering, Pearson Education, Fifth Edition, 2015.
- 3. Normon S. Nise, Control System Engineering, John Wiley sons, Eighth Edition, 2020.

Reference Books

- Madan Gopal, Control Systems Principles and Design, Tata McGraw hill, Seventh Edition, 2012.
- Ajit K.Mandal, Introduction to Control Engineering: Modeling, Analysis and Design, New Age International Publication, Second Edition, 2010.
- 3. S.Hasan Saeed, Automatic Control System, S.K. Kataria Sons, Ninth Edition, 2017.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.

Control Systems Laboratory (22PEET6052L)

Teaching Scheme Practical: 02 Hrs/Week Credit: 01

Examination Scheme

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

Pre-requisite:Knowledge of

- 1. Basic Electrical Engineering & Digital Electronics
- 2. Digital System Design
- 3. Engineering Mathematics-I
- 4. Engineering Mathematics-II

- 1. To provide fundamental concept of control systems.
- 2. To introduce mathematical modelling, time domain analysis frequency domain analysis.
- 3. To develop concepts of stability and its assessment criteria of the system.
- 4. To study basic concepts of controllers.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Understand the basic concepts of control system.	L1	Understand
CO2	Derive the mathematical model of different type of the systems.	L4	Analyze
CO3	Analysis of systems in time and frequency domain.	L4, L5	Analyze, Eval- uate
CO4	Understand & Find stability of given system using appropriate criteria.	L1	Understand
CO5	Apply the control theory to design the conventional controllers widely used in the industries.	L	Apply
List of Laboratory Experiments: (Any Eight)

- 1. To Verify the effect of zero and pole to the second order closed loop control system.
- 2. To find static errors for type 0, type 1, type 2 control System.
- 3. To plot frequency response of a 1^{st} order and 2^{nd} order control systems.
- 4. To find transfer function of a 1st order and 2nd order control systems.
- 5. To verify the effect of Zero and pole to open loop transfer function of a second order system with unity feedback.
- 6. To find controllability & observability of the given control system.
- 7. To design root locus for given control system.
- 8. To design Bode plot for first and second order control system.
- 9. To design Nyquist plot for given control system.
- 10. Verification of observability and controllability for given control system.
- 11. To find Transfer functions of P, PI, and PID controller.
- 12. To study Servo mechanism and characteristics of servo motor.
- 13. Any other experiment based on syllabus may be included, which would help the learner to understand topic/concept.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination of 25 Marks will be based on the entire syllabus including, the practicals performed during laboratory sessions.

Neural Networks & Fuzzy Logic (22PEET6053T)

Teaching Scheme

Lectures: 03 Hrs./Week Credit: 03

Examination Scheme

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite:Knowledge of

- 1. Engineering Mathematics-I
- 2. Engineering Mathematics-IV

- 1. To introduce the concepts and understanding of artificial neural networks and fuzzy logic.
- 2. To introduce neural network design concepts.
- 3. To expose neural networks based methods to solve real world complex problems.
- 4. To provide knowledge of fuzzy logic to design the real world fuzzy systems.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Explain training of Neural Networks using various training rules with consideration of different parameters like overfitting, under fitting.	L2	Understand
CO2	Calculate and update the weights of the neural networks to specify the working and applications of different types of neural networks.	L5	Evaluate
CO3	Design fuzzy sets for various applications and solve fuzzy set the- ory problems.	L6	Create
CO4	Design various engineering application using Neural Networks/ Fuzzy Logic.	L6	Create

Unit-I Introduction to Neural Networks 04 Hrs.

AIntroduction, Humans and Computers, Organization of the Brain, Biological Neuron, Biological and Artificial Neuron Models, Characteristics of ANN, McCulloch-Pitts Model, Potential Applications of ANN.

Unit-II Essentials of Artificial Neural Networks 04 Hrs.

Artificial Neuron Model, Operations of Artificial Neuron, Types of Neuron Activation Function, ANN Architectures, Classification Taxonomy of ANN Connectivity, Learning Strategy (Supervised, Unsupervised, Reinforcement), Learning Rules.

Unit-III Supervised Neural Networks 10 Hrs.

Feed forward neural network, Single-Layer feed forward architecture- Perceptron, Multiple-Layer feed forward architecture, Types of feed forward networks, Multi-layer perceptron, Training MLP: The back-propagation algorithm, Introduction to the concept of Support Vector Machine based classifier, GRADIENT-DESCENT algorithm, Generalization, Factors to be considered, Assessing the success of learning, Metrics for evaluation of classification method, Steps to use neural networks to data, Over fitting, Detecting over fit models: Cross validation.

Unit-IV Unsupervised Learning Neural Networks 10 Hrs.

Competitive Learning Networks Maxnet, Mexican Hat Net, Kohonen Self- Organizing Networks architecture, training algorithm, K-means, Radial Basis Function (RBF) neural network architecture and algorithm, and Discrete Hopfield networks.

Unit-V Fuzzy logic 06 Hrs.

Introduction to fuzzy logic, Basic Fuzzy logic theory, Fuzzy sets - properties operations, Fuzzy relation - Operations on fuzzy relations, Fuzzy Membership functions, Fuzzy Rules and Fuzzy Reasoning, Fuzzification and Defuzzification methods, Fuzzy Inference Systems, Mamdani Fuzzy Models, Fuzzy knowledge-based controllers, Sugeno Fuzzy Models.

Unit-VI Applications of Fuzzy Logic and Fuzzy Systems 06 Hrs.

Fuzzy pattern recognition, fuzzy C-means clustering, fuzzy image processing, Simple applications of Fuzzy knowledge-based controllers like washing machines, home heating system, and train break control.

Text Books

- S. N. Sivanandam and S. N. Deepa , Introduction to Soft computing, Wiley India Publications, Third Edition, 2018.
- 2. S. Rajasekaran, G.A. Vijayalakshmi Pai , Neural Networks, Fuzzy Systems And Evolutionary Algorithms: Synthesis And Applications, Second Edition , 2017.

Reference Books

- Thimothy J. Ross, Fuzzy Logic with Engineering Applications, Wiley India Publications, Third Edition, 2011.
- John Yen and Reza Langari, Fuzzy Logic- Intelligence, Control and Information, Pearson Publications, First Edition, 1998.
- J. S. R. Jang, C.T. Sun, and E. Mizutani, Neuro-Fuzzy and Soft Computing, PHI, First Edition, 1996.
- Simon Haykin, Neural Networks and Learning Machines, Pearson Education, Third Edition, 2016.
- 5. S. N. Sivanandam, S. Sumathi, and S. N. Deepa, Introduction to Neural Network Using Matlab, Tata McGraw-Hill Publications, First Edition, 2017.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.

Neural Networks & Fuzzy Logic Laboratory (22PEET6053L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01 **Examination Scheme**

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

Pre-requisite:Knowledge of

- 1. Engineering Mathematics-I
- 2. Engineering Mathematics-IV

- 1. To introduce the concepts and understanding of artificial neural networks and fuzzy logic.
- 2. To introduce neural network design concepts.
- 3. To expose neural networks based methods to solve real world complex problems.
- 4. To provide knowledge of fuzzy logic to design the real world fuzzy systems.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Explain training of Neural Networks using various training rules with consideration of different parameters like overfitting, under fitting.	L2	Understand
CO2	Calculate and update the weights of the neural networks to specify the working and applications of different types of neural networks.	L5	Evaluate
CO3	Design fuzzy sets for various applications and solve fuzzy set the- ory problems.	L6	Create
CO4	Design various engineering application using Neural Networks/ Fuzzy Logic.	L6	Create

List of Laboratory Experiments: (Any Eight)

- 1. Fuzzy Set Operations: AND, OR, D-Morgan's theorem.
- 2. (a)Simulation of Mamdani Fuzzy Inference System for washing machine control.(b) Summery of research paper based on Fuzzy logic.
- 3. Simulation of Sugeno Fuzzy Inference System for given application.
- 4. Simulation of Mamdani Fuzzy Inference System for image processing application. (Edge detection).
- 5. Write a program for perceptron training algorithm and test it for two input AND & OR gate function.
- 6. Write a program for training and testing of Multilayer Perceptron for two input EX-OR gate.
- 7. Write a program for training and testing of Multilayer Perceptron for character recognition application.
- 8. Program for Radial basis neural network for interpolation application.
- 9. Write a program for training and testing of RBF for pattern classification application.
- 10. Kohenan Self Organising map for image classification.
- To Study the use of Microsoft Machine Learning Studio (classic) in Neural Network and Machine Learning.
- 12. Case study.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination of 25 Marks will be based on the entire syllabus including, the practicals performed during laboratory sessions.

Teaching Scheme Lectures: 03 Hrs./Week Credit: 03

Examination Scheme

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite:Knowledge of

1. Structured Programming using C

- 1. To introduce operating system as a resource manager, its evolutions and fundamentals.
- 2. To help student understand concept of process and different process (linear and concurrent) Scheduling policies.
- 3. To help student familiar with memory, file and I/O management policies.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Understand the fundamental concepts of OS.	L1	Understand
CO2	Analyze the management policies adopted by processes, memory, File handling and I/O operations.	L4	Analyze
CO3	Apply the algorithms used for memory management, CPU scheduling and disk scheduling.	L3	Apply
CO4	Apply concepts related to deadlock to solve the problems.	L3	Apply
CO5	Analyze the functionalities of OS like Unix, Linux and Real Time Operating System.	L4	Analyze

Unit-I Fundamental of Operating System (OS) 04 Hrs.

Definition, objectives, functions, evolution, services, types, and different views of OS Operating System as a resource manager, system calls, and shell, Monolithic systems, layered systems, client server model, monolithic kernel and Microkernel.

Unit-II Process Management and Memory Management 08 Hrs.

Process, process creation, process control block, process states, process state transition diagram, Scheduling queues and schedulers, preemptive and non- preemptive scheduling algorithms, types of threads, multithreading models, Race condition, critical section, mutual exclusion, semaphores, monitors, Multiprogramming with fixed and variable partitions, memory allocation strategies, Logical and physical address space, paging and segmentation, Concept, performance of demand paging, page replacement algorithms, Deadlock Problem, deadlock characterization, deadlock prevention and deadlock avoidance deadlock detection and recovery.

Unit-III File Management and Input Output Management 08 Hrs.

File Naming, File Structure, File Types, File Access, File Attributes, File Operations, Memory Mapped Files, Implementing Files, contiguous allocation, linked list allocation, indexed allocations, Single level directory system, Two level directory system, Hierarchical Directory System, Principles of Input/output H/W: I/O Devices, Device Controllers, Direct Memory Access, Principles of Input/output S/W: Goals Of I/O S/W, Interrupt Handler, Device Driver, Device Independent I/O Software, Disks: RAID levels, Disks Arm Scheduling Algorithms, Management of free blocks.

Unit-IV Unix Operating System 08 Hrs.

History of UNIX, UNIX Goals, Unix Shell, interfaces to Unix, UNIX utility programs, Traditional UNIX Kernel, Modern UNIX Systems, Unix process management: Concept, Scheduling in Unix, Unix Memory management: Paging, Page replacement strategies, Unix file management: I-node, File allocation, I/O management, Unix Security measures.

Unit-V Linux Operating System 08 Hrs.

History, Linux Processes and Thread management, Scheduling in Linux, Linux System calls, Memory management: Virtual memory, Buddy Algorithm, Page replacement policy, Linux File System, I/O management: Disk Scheduling, Advantages of Linux and Unix over Windows.

Unit-VI Real Time Operating System (RTOS) 04 Hrs.

Introduction, Characteristics of real-time operating systems, Real Time task Scheduling, Modeling Timing constraints, Table-driven scheduling, Cyclic schedulers, Earliest Deadline First (EDF) scheduling, Rate Monotonic Algorithm. (RMA)

Text Books

- 1. Andrew Tanenbaum, Modern Operating Systems, PHI Publication, Third Edition, 2009.
- 2. William Stallings, Operating System-Internal Design Principles, Pearson, Sixth Edition, 2008.

Reference Books

- Silberschatz A., Galvin P., and Gagne G, Operating Systems Concepts, Wiley, Eight Edition, 2009.
- Richard Blum and Christine Bresnahan, Linux Command Line Shell Scripting, Wiley, Second Edition, 2011.
- 3. Rajib Mall, Real-Time Systems: Theory and Practice, Pearson, First Edition, 2009.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.

Operating Systems Laboratory (22PEET6054L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01 **Examination Scheme** Teacher Assessment: 25 Marks End Sem Exam : 25 Marks

Total: 50 Marks

Pre-requisite:Knowledge of

1. Structured Programming using C

- 1. To introduce operating system as a resource manager, its evolutions and fundamentals.
- 2. To help student understand concept of process and different process (linear and concurrent) Scheduling policies.
- 3. To help student familiar with memory, file and I/O management policies.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Understand the fundamental concepts of OS.	L1	Understand
CO2	Analyze the management policies adopted by processes, memory, File handling and I/O operations.	L4	Analyze
CO3	Apply the algorithms used for memory management, CPU scheduling and disk scheduling.	L3	Apply
CO4	Apply concepts related to deadlock to solve the problems.	L3	Apply
CO5	Analyze the functionalities of OS like Unix, Linux and Real Time Operating System.	L4	Analyze

List of Laboratory Experiments: (Any Eight)

- 1. To implement Linux commands.
- 2. To implement Linux shell script.
- 3. To implement any one the basic commands of Linux like ls, cp, mv and others using kernel APIs.
- 4. To implement preemptive and non-preemptive algorithms.
- 5. To implement concept of deadlock.
- 6. To implement concept of memory management.
- 7. To implement demand and virtual memory implementation.
- 8. To implement file allocation strategies.
- 9. To implement disk scheduling techniques.
- 10. To implement file organization techniques.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination of 25 Marks will be based on the entire syllabus including, the practicals performed during laboratory sessions.

Teaching Scheme Lectures: 03 Hrs./Week Credit: 03

Examination Scheme

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite:Knowledge of

1. Data Base Management System laboratory

- 1. To Provide an Overview of an exciting growing field of Big Data Analytics.
- 2. To introduce the tools required to manage and analyze big data like Hadoop, NoSql, Map Reduce, Spark.
- 3. To teach the fundamental techniques in achieving big data analytics with scalability and streaming capability.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Understand the key issues in big data management and its asso- ciated applications for business decisions and strategy.	L2	Comprehension
CO2	Understand and Develop problem solving and critical thinking skills in fundamental enabling techniques like Hadoop and NoSQL in big data analytics.	L2	Comprehension
CO3	Evaluate Big Data processing by using Map Reduce.	L5	Evaluate
CO4	Interpret business models and scientific computing paradigms and apply software tools for big data analytics.	L3	Apply
CO5	Exploring the capabilities of big data using Apache Spark.	L3	Apply

Unit-I Introduction to Big Data Analytics Hadoop 08 Hrs.

Introduction to Big Data, Big Data characteristics, Types of Big Data, Traditional vs. Big Data business approach. Technologies available for Big Data, Infrastructure for Big Data, Big Data challenges, Case Study of Big Data solutions. Duration 08 Introduction to Hadoop, Core Hadoop components, Hadoop Ecosystem, Physical architecture, Hadoop limitations.

Unit-II NoSQL 07 Hrs.

NoSQL: Introduction to NoSQL, NoSQL business drivers, NoSQL case studies. NoSQL data architecture patterns: Key-value stores, Graph stores, Column family (Bigtable) stores, Document stores, Variations of NoSQL architectural patterns. Analysing big data with a shared-nothing architecture, Choosing distribution models, master-slave versus peer-to-peer Introduction to MongoDB, MongoDB commands.

Unit-III MapReduce 08 Hrs.

MapReduce and The New Software Stack: Distributed File Systems, Physical organization of compute Nodes, Large scale file-system organization. MapReduce: The Map tasks, grouping by key, The Reduce tasks, Combiners, Details of MapReduce execution, Coping with node failures. Matrix vector multiplication using MapReduce, Relational operators using MapReduce.

Unit-IV Techniques in Big Data Analytics 10 Hrs.

Finding Similar Item: Nearest Neighbour Search, Similarity of Documents. Mining Data Streams: Data Stream Management Systems, Data Stream Model, Examples of Data Stream Applications: Sensor Networks, Network Traffic Analysis. Frequent Item set Mining: Market Basket Model-Applications, Association Rule- Confidence, Interest, Support, Apriori Algorithm - Pass1, Pass2 Recommendation Systems: Introduction, Collaborative-Filtering System, Content based recommendation system Link analysis: Page rank algorithm, Structure of web.

Unit-V Big Data Analytics using Apache Spark 07 Hrs.

Introduction to Spark: Features, Spark built on Hadoop, Components of Spark. Resilient Distributed Datasets: Data sharing using Spark RDD, Iterative operations on Spark RDD, Interactive operations on Spark RDD, RDD transformations, Execution of word count transformation.

Text Books

- 1. Radha Shankarmani, M Vijayalakshmi, Big Data Analytics, Wiley, Second Edition, 2016.
- 2. Alex Holmes, Hadoop in Practice, Manning Press, Dreamtech Press, Second Edition, 2015.

3. Holden Karau, Andy Konwinski, Matei Zaharia, Learning Spark OReilly, Second Edition, 2015.

Reference Books

- 1. Bart Baesens, Analytics in a Big Data World: The Essential Guide to Data Science and its Applications, Wiley Big Data Series, Edition, 2017.
- 2. Vignesh Prajapati, Big Data Analytics with R and Hadoop, Packt Publishing Limited First Edition, 2013.
- 3. Tom White, Hadoop: The Definitive Guide, O'Reilly Publications, Second Edition, 2016.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.

Big Data Analytics Laboratory (22PEET6055L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01 Examination Scheme

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

Pre-requisite:Knowledge of

1. Data Base Management System laboratory

- 1. To Provide an Overview of an exciting growing field of Big Data Analytics.
- 2. To introduce the tools required to manage and analyze big data like Hadoop, NoSql, Map Reduce, Spark.
- 3. To teach the fundamental techniques in achieving big data analytics with scalability and streaming capability.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Understand the key issues in big data management and its asso- ciated applications for business decisions and strategy.	L2	Comprehension
CO2	Understand and Develop problem solving and critical thinking skills in fundamental enabling techniques like Hadoop and NoSQL in big data analytics.	L2	Comprehension
CO3	Evaluate Big Data processing by using Map Reduce.	L5	Evaluate
CO4	Interpret business models and scientific computing paradigms and apply software tools for big data analytics.	L3	Apply
CO5	Exploring the capabilities of big data using Apache Spark.	L3	Apply

List of Laboratory Experiments: (Any Eight)

- 1. Forward Kinematics
- Downloading and installing Hadoop; Understanding different Hadoop modes. Startup scripts, Configuration files.
- 3. Execution of Hadoop file handling commands.
- 4. Installation of MongoDB and execution of CREATE, INSERT, DELETE and UPDATE operations.
- 5. Querying in MongoDB using FIND command, aggregate functions etc.
- 6. Designing of graphical data store and querying in Neo4j.
- 7. Execution of PIG SCRIPTING language.
- 8. Execution of HIVE SCRIPTING language.
- 9. Execution of Matrix Multiplication Using MapReduce.
- 10. Execution of Word Count using MapReduce.
- 11. Execution of Word Count using Apache Spark.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination of 25 Marks will be based on the entire syllabus including, the practicals performed during laboratory sessions.

Teaching Scheme

Lectures: 03 Hrs./Week Credit: 03

Examination Scheme

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite:Knowledge of

- 1. Electromagnetic Wave Propagation
- 2. Analog Communication

Course Objectives

- 1. To interpret Radar equations.
- 2. To explain different types of radar.

3. To design Radar transmitters and receivers for given conditions.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Understand generalized concept of RADAR & its applications.	L2	Understand
CO2	Solve problems using radar equations.	L3	Apply
CO3	Describe different types of radar for specific application.	L2	Understand
CO4	Explain concept of tracking radar.	L2	Understand
CO5	Evaluate the design constraints for transmitter and receiver.	L6	Evaluate

Unit-I Introduction to Radar 08 Hrs.

Basic Radar, basic ranger equation, Block Diagram, Radar Frequencies, Applications of Radar.

Unit-IIRadar Range Equation08 Hrs.

Detection of signal in noise, Receiver Noise and Signal-to-noise Ratio, Probability of detection and false alarm: Simple, complex Targets, Pulse Repetition Frequency.

Unit-III MTI and Pulse Doppler Radar 10 Hrs.

Introduction to Doppler and MTI radar, Doppler frequency shift, Simple CW Doppler radar, MTI radar block diagram, Delay line canceler, Moving- target-detection, Pulse Doppler radar.

Unit-IV Tracking Radar 06 Hrs.

Mono pulse tracking, Conical scan and sequential lobbing, Limitation of tracking accuracy, Low angle tracking.

Unit-V Radar Transmitter and Receiver 08 Hrs.

Radar RF power sources: Klystron, Travelling wave tube, Magnetron, Low power transmitter, high power transmitter, Advantages of solid state RF power source, Duplexer, and Mixer and their types, Receiver noise figure, Radar Display: Types of displays.

Text Books

- 1. Merrill Skolnik, Introduction to Radar Systems, Tata McGra Hill, Second Edition, 2010
- 2. G S N Raju, Radar Engineering, Willey publication, First Edition, 2020.
- 3. Bassem R. Mahafza, Radar Signal Analysis, CRC press, First Edition, 2021.

Reference Books

- E David Jansing, Introduction to Synthetic Aperture Radar, McGraw Hill, Second Edition, 2021.
- 2. Clive Alabaster, Pulse Doppler Radar, SciTech Publishing, Second Edition, 2012.
- William L Melvin, James A Scheer, Principals of Modern Radar, SciTech Publishing, First Edition, 2014.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.

Radar Engineering Laboratory (22PEET6056L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01 **Examination Scheme**

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

Pre-requisite:Knowledge of

- 1. Electromagnetic Wave Propagation
- 2. Analog Communication

- 1. To interpret Radar equations.
- 2. To explain different types of radar.
- 3. To design Radar transmitters and receivers for given conditions.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Understand generalized concept of RADAR & its applications.	L2	Understand
CO2	Solve problems using radar equations.	L3	Apply
CO3	Describe different types of radar for specific application.	L2	Understand
CO4	Explain concept of tracking radar.	L2	Understand
CO5	Evaluate the design constraints for transmitter and receiver.	L6	Evaluate

List of Laboratory Experiments: (Any Eight)

- 1. To study basic Radar and range equation.
- 2. To Study CW Radar and find the relative speed of the object.
- 3. Derive Radar range equation with noise figure and find the distance.
- 4. To study MTI Radar and find the blind speed.
- 5. Calculate pulse repetition frequency and velocity of the moving object.
- 6. To study various displays used in Radar systems.
- 7. To study clutters and its effects on Radar range equation.
- 8. To study delay line canceller.
- 9. Find the speed of the fan using Doppler Radar.
- 10. To study duplexer and mixer.
- 11. To study tracking Radar.

Evaluation Scheme:

Continuous Assessment (A):

Laboratory work shall consist of minimum 8 experiments and subject specific lab assignment/case study/mini project.

The distribution of marks shall be as follows:

- 1. Performance in Experiments: 05 Marks
- 2. Journal Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination of 25 Marks will be based on the entire syllabus including, the practicals performed during laboratory sessions.

Teaching Scheme Lectures: 03 Hrs./Week Credit: 03

Examination Scheme

Term Test: 15 Marks Teacher Assessment: 20 Marks End Sem Exam: 65 Marks Total: 100 Marks

Pre-requisite:Knowledge of

1. Engineering Mathematics-IV

- 1. Understanding basic concepts of linear algebra to illustrate its power and utility through applications.
- 2. Apply the concepts of vector spaces, linear transformations, matrices and inner product spaces in Engineering.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Explain the abstract concepts of matrices and system of linear equations using decomposition Methods.	L2	Understand
CO2	Demonstrate the basic notion of vector spaces and subspaces.	L3	Apply
CO3	Apply the concept of vector spaces using linear transforms and inner product spaces applications in cryptography.	L3	Apply

Unit-I System of Linear Equations 06 Hrs.

Gaussian elimination and Gauss Jordan method Elementary matrices Permutation matrix inverse matrices System of linear equations LU factorizations.

Unit-II Vector Spaces 12 Hrs.

The Euclidean space and vector space, subspace linear combination, span-linearly dependent-independent bases, dimensions, finite dimensional vector space, The four fundamental spaces, Rank and nullity Bases for subspace.

Unit-III Linear Transformations 10 Hrs.

Linear transformations, Basic properties, invertible linear transformation, matrices of linear transformations, vector space of linear transformations, change of bases

Unit-IV Inner Product Spaces and Applications 07 Hrs.

Dot products and inner products, the lengths and angles of vectors, matrix representations of inner products, Gram-Schmidt Orthogonalisation, QR factorization- Projection - orthogonal projections

Unit-V Applications 05 Hrs.

An Introduction to coding - Classical Cryptosystems Plain Text, Cipher Text, Encryption.

Text Books

- 1. Jin Ho Kwak and Sungpyo Hong, Linear Algebra, Springer, Second Edition, 2004.
- Bernard Kolman and David, R., Introductory Linear Algebra- An applied first course, Pearson Education, Ninth Edition, 2011.

Reference Books

- Stephen Andrilli and David Hecker, Elementary Linear Algebra, Academic Press, Fifth Edition, 2016.
- 2. Rudolf Lidl and Guter Pilz,, Applied Abstract Algebra, Springer, Second Edition, 2004.
- 3. Howard Anton, Robert C Busby, Contemporary linear algebra, Wiley, First Edition, 2003.
- 4. Gilbert Strang, Introduction to Linear Algebra, Cengage Learning, Fifth Edition, 2015.

Evaluation Scheme:

Continuous Assessment (A):

Subject teacher will declare Teacher Assessment criteria at the start of semester.

Continuous Assessment (B):

- 1. Two term tests of 15 marks each will be conducted during the semester.
- 2. Total duration allotted for writing each of the paper is 1 hr.
- 3. Best of both the tests will be considered for final grading.

End Semester Examination (C):

- 1. Question paper will be based on the entire syllabus summing up to 65 marks.
- 2. Total duration allotted for writing the paper is 3 hrs.

Linear Algebra Laboratory/Tutorial (22PEET6057L)

Teaching Scheme

Practical: 02 Hrs/Week Credit: 01 Examination Scheme

Teacher Assessment: 25 Marks End Sem Exam : 25 Marks Total: 50 Marks

Pre-requisite:Knowledge of

1. Engineering Mathematics-IV

- 1. Understanding basic concepts of linear algebra to illustrate its power and utility through applications.
- 2. Apply the concepts of vector spaces, linear transformations, matrices and inner product spaces in Engineering.

COs	Course Outcomes	Blooms Level	Blooms De- scription
CO1	Explain the abstract concepts of matrices and system of linear equations using decomposition Methods.	L2	Understand
CO2	Demonstrate the basic notion of vector spaces and subspaces.	L3	Apply
CO3	Apply the concept of vector spaces using linear transforms and inner product spaces applications in cryptography.	L3	Apply

List of Tutorial : (Any Eight)

- 1. Gaussian elimination and Gauss Jordan Method
- 2. LU Factorizations.
- 3. The Four Fundamental Spaces.
- 4. Linear Transformations.
- 5. Gram-Schmidt Orthogonalisation.
- 6. QR Factorization.
- 7. Linear Dependence and Independence.
- 8. Least Squares Approximation.
- 9. Case Study: Classical Cryptosystems.

Evaluation Scheme:

Continuous Assessment (A):

Tutorial work shall consist of minimum 8 tutorials from the above suggested list or any other experiment//tutorial based on syllabus to be included, which would help the learner to apply the concept learnt. The distribution of marks shall be as follows:

- 1. Performance in Tutorial: 05 Marks
- 2. Tutorial Submission: 05 Marks
- 3. Viva-voce: 05 Marks
- 4. Subject Specific Lab Assignment/Case Study/mini project: 10 Marks

The final certification and acceptance of laboratory journal/manual/report will be subject to satisfactory performance of Tutorial work and upon fulfilling minimum passing criteria in the teacher assessment.

End Semester Examination (C):

Oral / Practical examination of 25 Marks will be based on the entire syllabus including, the Tutorial work/practicals performed during Tutorial/laboratory sessions.

Professional Business Communication Tutorial (22HMET6060T)

Teaching Scheme	Examination Scheme
Tutorial: 02 Hrs/Week	Teacher Assessment: 50 Marks
Credit: 2	Total: 50 Marks

Pre-requisite: Basic course in Effective Communication Skills

- 1. To inculcate professional and ethical attitude at the workplace.
- 2. To enhance communication and interpersonal skills.
- 3. To develop effective employability skills.
- 4. To hone written skills for technical documentation.

COs	Course Outcomes	Blooms Level	Blooms Description
CO1	Prepare technical documents using appropriate style, format, and language	L6	Create
CO2	Use employability skills to optimize career opportunities	L3	Apply
CO3	Employ storytelling techniques in corporate situations	L3	Apply
CO4	Conduct effective meetings and document the process	L2 and L3	Understand, Apply
CO5	Demonstrate interpersonal skills in professional and personal sit- uations	L3	Apply
CO6	Describe cultural differences, etiquettes, and the concept of pro- fessional ethics	L3	Apply

Unit-I Technical Writing 06 Hrs.

Report Writing: Types of reports, Basic structure of a report, collection of data through questionnaires, survey analysis, language and style in reports Business Proposal Writing: Types of business proposals, format of proposal, language and style, presentation of proposal Plagiarism: Types of plagiarism, consequences of plagiarism.

Unit-II Employment Skills 08 Hrs.

Group Discussion: Purpose of a GD, types of GD, criteria for evaluating GD, Dos and Donts of GD Resume Writing: Types of resumes, structure, content and formatting of resume. Interview Skills: Types and modes of interview, Preparation for interview, Dos and Donts of interview, frequently asked questions during interview Presentation Skills: Presentation strategies, overcoming stage fear, techniques to prepare effective Power Point presentation.

Unit-III Corporate Story Telling 03 Hrs.

Basics of storytelling: Setting, characters, plot, crisis, climax, resolution, Benefits of storytelling Types of stories: Elevator pitch, product stories, event stories, stories in presentations, storytelling in SOPs and interviews, storytelling to manage conflict or to motivate Storytelling techniques: Narration using verbal and non-verbal communication, Analysis of storytelling strategies of corporate master storytellers.

Unit-IV Meetings and Documentation 02 Hrs.

Planning and preparation for meetings: Planning layout of meetings, arranging logistics, defining roles and responsibilities Strategies for conducting effective meetings: Follow the agenda, record discussion, observe meeting decorum Documentation: Draft notice, agenda and minutes of meeting Business meeting etiquettes: Verbal and non-verbal aspects of etiquettes.

Unit-V Introduction to Interpersonal Skills 05 Hrs.

Emotional Intelligence: Definition, difference between IQ and EQ, how to develop EQ Leadership: Types of leadership, leadership styles, case studies Team Building: Difference between group and team, importance of teamwork, strategies to be a good team player Time Management: Importance of time management, cultural views of time, 80/20 rule, time wasters, setting priorities and goals, Conflict Management: Types of conflicts, strategies to manage conflict, case studies.

Unit-VI Cross-cultural communication and Professional ethics 02 Hrs.

Communication across cultures: Understanding cultures and developing sensitivity towards cultural differences Corporate etiquettes: Telephone, dining, cubicle etiquette, etc. Professional ethics: Effective work habits, accountability, integrity and excellence.

Note:Laboratory (conducted batch wise) will comprise of activities and assignments based on the syllabus.

Text Books

- 1. Fred Luthans, "Organizational Behavior", McGraw Hill, edition
- 2. Lesiker and Petit, "Report Writing for Business", McGraw Hill, edition.
- 3. Huckin and Olsen, "Technical Writing and Professional Communication", McGraw Hill.
- Wallace and Masters, "Personal Development for Life and Work", Thomson Learning, 12th edition
- 5. Heta Murphy, "Effective Business Communication", Mc Graw Hill, edition
- 6. Sharma R.C. and Krishna Mohan, "Business Correspondence and Report Writing", Tata McGraw-Hill Education
- 7. Ghosh, B. N., "Managing Soft Skills for Personality Development", Tata McGraw Hill.Lehman
- 8. Bell, Smith, "Management Communication" Wiley India Edition, 3rd edition
- 9. Dr. Alex, K., "Soft Skills", S Chand and Company
- 10. Subramaniam, R., "Professional Ethics" Oxford University Press
- Sandeep Das, "How Business Story Telling Works: Increase Your Influence and Impact" Penguin Random House India Pvt. Ltd.

Evaluation Scheme:

Laboratory: (Term work):

Term work shall consist of 6 assignments, Group Discussion and Power Point Presentation based on the Business Proposal.

The distribution of marks for term work shall be as follows:

- 1. Tutorials = (25) Marks.
- 2. Business Proposal = (15) Marks.

- 3. Group Discussion = (10) Marks.
- 4. TOTAL: (50) Marks.

The final certification and acceptance of term work will be subject to satisfactory performance of laboratory work and upon fulfilling minimum passing criteria in the term work.

Practical Scheme Practical : 02 Hrs./week Credit : 02

Examination Scheme

Teacher Assessment : 25 Marks End Sem Exam : 25 Marks Total : 50 Marks

Pre-requisite:Knowledge of

- 1. Analog Communication
- 2. Microcontroller Applications-I and II

- To develop the team building, writing, logical reasoning and management skills. .
- To provide the connections between the designs and concepts across different disciplinary boundaries.
- To encourage students to become independent personnel, critical thinkers and lifelong learners.

СО	Course Outcomes	Blooms Level	Blooms Description
CO1	Apply engineering knowledge to produce solution of a prob- lem considering cultural, social, environmental, and eco- nomic factors using appropriate tool and method.	L4	Analyze
CO2	Demonstrate project based learning that allows students to transfer existing ideas into new applications.	L2	Understand
CO3	Develop an ability to work in teams and manage the conduct of the research study.	L3	Apply
CO4	Integrate different perspectives from relevant disciplines which help them to get internships, jobs and admission for higher studies.	L3	Apply
CO5	Present the research in the form of technical writing, under- stand what constitutes to plagiarism and how to use proper referencing styles.	L2	Understand

Syllabus:

Domain knowledge (any beyond) needed from the following areas for the effective implementation of the project:

Microcontroller and Embedded Systems, Signal Processing, Microwave and Antennas, Networking and Internet of Things, Data science and Big data, Communication, Web and Application development, Robotics, AI and Machine learning, etc.

The above areas can be updated based on the technological innovations and development needed for specific project.

Guidelines:

The main purpose of this activity is to improve the students' technical skills, communication skills by integrating writing, presentation and teamwork opportunities.

- Each group will be reviewed twice in a semester and marks will be allotted based on the various points mentioned in the evaluation scheme.
- In the first review of this semester, each group is expected to complete 30 % of project.
- $\bullet\,$ In the second review of this semester, each group is expected to complete 50 % of project.
- Interaction with alumni mentor will also be appreciated for the improvement of project.

Assessment Criteria:

- At the end of the semester, after confirmation by the project guide, each project group will submit project completion report in prescribed format for assessment to the departmental committee (including project guide).
- Assessment of the project stage I (at the end of the semester) will be done by the departmental committee (including project guide).
- Oral examination should be conducted by Internal and External examiners. Students have to give presentation and demonstration based on their project.

Prescribed project report guidelines:

Size of report shall be of minimum 25 pages. Project Report should include appropriate content for:

- Introduction
- Literature Survey
- Related Theory
- Implementation details

- Project stage I Outcomes
- Conclusion
- References

Assessment criteria for the departmental committee (including project guide) for Continuous Assessment:

Assessment criteria for the departmental committee (including project guide) for End Semester Exam:

Each group will be reviewed twice in a semester by faculty guide and faculty coordinator based on the following criteria:

- Project progress
- Documentation/Technical paper writing
- Key findings
- Validation of results

Each review consists of 25 marks. Average of the marks scored in both the two reviews will be considered for final grading. The final certification and acceptance of TA ensures the satisfactory performance on the above aspects.

Sr	Week (Start Date:End Date)	Work Done	Sign of Guide	Sign of Coordinator
1				
2				

Table 1: Log Book Format

Table 2: Continuous Assessment Sheet

Sr	Exam Seat No	Name of Student	Student Attendance	Log Book Maintain	Literature Review	Depth of Un- derstanding	Report	Total
			5	5	5	5	5	25

'	Table	3:	Eval	uation	Sheet

Sr	Exam Seat No	Name of Student	Project Selection	Design/ Simulation/ Logic	PCB/ hardware/ program- ming	Result Ver- ification	Presentation	Total
			5	5	5	5	5	25