
		<b>R C Patel Institute of Technology, Shirpur</b> <b>An Autonomous Institute</b>						
<b>Department of Electronics &amp; Telecommunication</b>		<b>Department of Electronics &amp; Telecommunication</b>						
<b>TIME TABLE</b>								
<b>Class: TE E &amp; TC (B)</b>		<b>Year: 2024-25 Sem I</b>		<b>Class Room No. CR-86</b>				
		<b>with effect from 5/8/2024</b>						
SR.No	Time	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	
1	10.30 To 11.30	RFCD_MBD	DSP_ABJ	MCA-II_THJ	DSA_NLL	DSA_NLL	AC_B2 MCA-II_B1 DSA_B3	
2	11.30 To 12.30	EE_PGP	RFCD_MBD	RFCD_MBD	RFCD_MBD	DSP_ABJ		
	12.30 To 1.30	LUNCH BREAK						
3	1.30 To 2.30	DSP_ABJ	MCA-II_THJ	AC_B1_JPP RFCD B2_PRB	DSP_ABJ	AC_JPP	MCA-II_THJ	
4	2.30 To 3.30	AC_JPP	AC_JPP		AC_JPP	MCA-II_THJ	PROJECT-Hrs	
	3.30 To 3.45	SHORT RECESS						
5	3.45 To 4.45	RFCD B3 DSP_B1 DSA_B2	RFCD B1 DSP_B2 AC_B3	DSA_B1 DSP_B3	MCA II_B3	MCA II_B2	PROJECT-Hrs	
6	4.45 To 5.45						PROJECT-Hrs	
		<b>SUBJECT</b>	AC	RFCD	MCA-II	DSP	DSA	EE
		<b>TH</b>	Prof J P Patil	Dr M B Dembrani	Dr T H Jaware	Prof A B Jaiswal	Prof N L Lokhande	Dr P G Patil
		<b>PR/TUT</b>	Prof J P Patil	Prof P R Bhole	Dr T H Jaware	Dr J B Jadhav	Prof M L Patel	-----
<b>Note: 80 % attendance is compulsory. Candidate failing to complete this will be detained</b>								
<b>HOD</b> Prof Dr V S Patil		<b>Time Table Incharge</b> Prof P M GOAD						