



Sem-II

Year: 2024-25

Class: T Y.B. TECH (ELECTRICAL)

(Class Room No-02)

Sr.No	Time	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday	
1	10.30-11.20	PE(NSM)	PE(NSM)	PSOC(YKK)	PBC(T)-T1	EMD(SYS)	PBC(T)-T2	
2	11.20-12.10	CS(NRA)	EMD(SYS)	PE(NSM)	PSOC(YKK)	CS(NRA)	PE(NSM)	
3	12.10-01.00	LUNCH BREAK						
4	01.00-01.50	EMD-A1(SYS) PE-A2(NSM) PSOC-A3(YKK)	PSOC-A1(YKK) CS-A2(NRA) PROJECT-I-A3	CS-A1(SYS) PSOC-A2(YKK) PLC-A3(NRA)	PE-A1(YKK) PROJECT-I-A2 CS-A3(BPP)	PLC-A1(SAP) EMD-A2(SYS) PE-A3(YKK)	PROJECT-I-A1 PLC-A2(SAP) EMD-A3(SYS)	
5	01.50-02.40							
6	02.40-02.55	SHORT BREAK						
7	02.55-03.45	EMD(SYS)	PLC(SAP)	PLC(SAP)	CS(NRA)	PSOC(YKK)	CS(NRA)	
8	03.45-04.35	EMD(SYS)	PLC(SAP)	PLC(SAP)	PROJECT-I	LG MEETINGS	PSOC(YKK)	
9	04.35-05.35	CODE CHEF PREPARATION (PROF.BPP)	CODE CHEF PREPARATION (PROF.RSP)	CODE CHEF PREPARATION (PROF.YKK)	CODE CHEF PREPARATION (PROF.BPP)	CODE CHEF PREPARATION (PROF.RSP)	CODE CHEF PREPARATION (PROF.YKK)	
10	05.35-07.30	CAMPUS PLACEMENT PREPARATION						

Abbreviation: 1)CS-Control Systems 2)PE-Power Electronics 3)EMD-Electrical Machind Design 4)PSOC-Power System Operation & Control 5)PLC-Programmable Logic control 6)PBC-professional Buiseness & Communication

Faculty-1) CS-Prof. Dr. N R Ansari 2) PE-Prof.N S Mahajan 3) EMD-Prof.S. Y. Sayais 4) PSOC-Prof.Dr.Y K Kirange

5)PLC-Prof.Dr.S.A.Patil 6) PBC-Prof.R.S Patil 7)CODE CHEF(Coordinator)- Prof.H O.Borase

Note: 80 % attendance is compulsory candidate failing to complete this will be detained.

CLASS TEACHER

PROF R S PATIL

Time Table Incharge

PROF R S PATIL

HOD, Electrical Engineering
 R C Patel Institute of Technology
 Shirpur, Dist. Dhule (MS)

Director

PROF DR. J B PATIL