



R C Patel Institute of Technology (An Autonomous Institute), Shirpur

Department of Electrical Engineering

TIME TABLE (w.e.f.01/02/2025)

Class: B.TECH (ELECTRICAL)



Year: 2024-25

Sem-II

(Class Room No-03)

Sl.No	Time	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday
1	10.30-11.20	DCM & CS	DCM & CS	DCM & CS	DCM & CS	PROJECT/ INTERNSHIP HRS	PROJECT/ INTERNSHIP HRS
2	11.20-12.10	HVDC	HVDC	HVDC	HVDC		
3	12.10-01.00	LUNCH BREAK					
4	01.00-01.50	PROJECT/INTERNSHIP HRS					
5	01.50-02.40	PROJECT/INTERNSHIP HRS					
6	02.40-02.55	SHORT BREAK					
7	02.55-03.45	PROJECT/INTERNSHIP HRS					
8	03.45-04.35	PROJECT/INTERNSHIP HRS					
9	04.35-07.30	CAMPUS PLACEMENT PREPARATION					

Abbreviation: 1)HVDC(Elective)-High Voltage DC Transmission 2)DCM & CS (Elective)-DC Microgrid & Control System
Faculty-1) HVDC-Prof.B P Patil 2) DCM & C-Prof.K J Gandhi 3) PROJECTS/INTERNSHIP- All Guides/Mentor

Note: 80 % attendance is compulsory candidate failing to complete this will be detained.

CLASS TEACHER PROF.DR.Y K KIRANGE

HoD

Time Table Incharge

Director

PROF. DR. S. A. PATIL
Department of Electrical Engineering

PROF. R. S. PATIL

PROF. DR. J. B. PATIL

Shirpur, Dist: Dhule (MS)